R1294L-Y Series

3 ch DC/DC for TFT LCD for Industrial Applications

OUTLINE

The R1294L is the optimized DC/DC converter IC for TFT LCD displays. R1294L contains one PWM step-up DC/DC converter controller and two diode charge-pump controllers. The charge-pumps can control a boost output and a negative output and have the output voltage regulation function with external resistors. The power-on sequence can be made with setting the delay time with external capacitors for each charge pump channel.

This is a high-reliability semiconductor device for industrial applications (-Y) that has passed both the screening at high temperature and the reliability test with extended hours.

FEATURES

- Input Voltage Range (Maximum Rating) ........... R1294L101A: 2.0 V to 5.5 V (6.5 V)
  .......... R1294L102A: 2.5 V to 5.5 V (6.5 V)
  .......... R1294L103A: 3.3 V to 5.5 V (6.5 V)
- Temperature Coefficient of VFB ($\Delta V_{FB}/\Delta T$) .......... Typ. ±150 ppm/°C (−40°C ≤ Ta ≤ 105°C)
- Temperature Coefficient of VREF ($\Delta V_{REF}/\Delta T$) .......... Typ. 150 ppm/°C
- Temperature Coefficient of CPPFB ($\Delta V_{PPFB}/\Delta T$) .......... Typ. 150 ppm/°C (−40°C ≤ Ta ≤ 105°C, CPVCC = 9 V)

[Step-up DC/DC Controller]
- Built-in 2 A Nch-switch ($R_{ON} = 150 \, \text{m} \Omega$ Typ.)
- Overcurrent Protection
- Adjustable $V_{OUT}$ up to 20 V with external resistors
- Adjustable Phase compensation with external components
- Maxduty adjustable with external resistors for DTC pin
- Soft-start time adjustable with external capacitor for SS pin
- Oscillator Frequency: Adjustable frequency with resistors (210 kHz to 1400 kHz)

[Charge-pump]
- Adjustable output voltage with external resistors
- Sequence function: Charge-pump turns on after the main step-up converter voltage outputs. The positive charge-pump and the negative charge-pump turn-on sequence control is adjustable by setting delay time for each channel
- Oscillator Frequency: 1/4 of the main step-up DC/DC converter oscillator frequency

[Controller]
- Under Voltage Lock-Out (UVLO: selectable detector threshold from 1.8 V/2.2 V/2.8 V)
- Reference Voltage (VREF: Typ.1.2 V)
- Short Protection with timer latch function (adjustable delay time with external capacitor)
- Shutdown all the outputs if at least one of three outputs is shorted to the GND.
- Stand-by function by CE pin
- Package......................................................... Thin 24-pin package QFN0404-24B
R1294L-Y
No. EY-368-180326

APPLICATIONS

- Industrial equipments such as FAs and smart meters
- Equipments used under high-temperature conditions such as surveillance camera and vending machine
- Equipments accompanied by self-heating such as motor and lighting

SELECTION GUIDE

The UVLO threshold voltage is user-selectable.

<table>
<thead>
<tr>
<th>Product Name</th>
<th>Package</th>
<th>Quantity per Reel</th>
<th>Pb Free</th>
<th>Halogen Free</th>
</tr>
</thead>
<tbody>
<tr>
<td>R1294L10xA-E2-Y</td>
<td>QFN0404-24B</td>
<td>1,000 pcs</td>
<td>Yes</td>
<td>Yes</td>
</tr>
</tbody>
</table>

x: Specify the UVLO threshold voltage

1: 1.8 V
2: 2.2 V
3: 2.8 V
R1294L Block Diagram
**PIN DESCRIPTIONS**

<table>
<thead>
<tr>
<th>Pin No.</th>
<th>Symbol</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>PGND</td>
<td>Power GND Pin</td>
</tr>
<tr>
<td>2</td>
<td>PGND</td>
<td>Power GND Pin</td>
</tr>
<tr>
<td>3</td>
<td>AGND</td>
<td>Analog GND Pin</td>
</tr>
<tr>
<td>4</td>
<td>VIN</td>
<td>Power Input Pin</td>
</tr>
<tr>
<td>5</td>
<td>VREF</td>
<td>Reference Voltage Output Pin</td>
</tr>
<tr>
<td>6</td>
<td>CE</td>
<td>Chip Enable Pin</td>
</tr>
<tr>
<td>7</td>
<td>VFB</td>
<td>Step-up DC/DC Feedback Pin</td>
</tr>
<tr>
<td>8</td>
<td>SS</td>
<td>Step-up DC/DC Soft-start Pin</td>
</tr>
<tr>
<td>9</td>
<td>TST</td>
<td>TEST Pin</td>
</tr>
<tr>
<td>10</td>
<td>DTC</td>
<td>Step-up DC/DC Maxduty Setting Pin</td>
</tr>
<tr>
<td>11</td>
<td>DELAY</td>
<td>Short Protection Delay Setting Pin</td>
</tr>
<tr>
<td>12</td>
<td>AMPOUT</td>
<td>Amplifier Output Pin For Phase Compensation</td>
</tr>
<tr>
<td>13</td>
<td>RT</td>
<td>Oscillator Frequency Setting Pin</td>
</tr>
<tr>
<td>14</td>
<td>CPNDLY</td>
<td>Negative Charge-pump Delay Setting Pin</td>
</tr>
<tr>
<td>15</td>
<td>CPNFB</td>
<td>Negative Charge-pump Feedback Pin</td>
</tr>
<tr>
<td>16</td>
<td>CPPDLY</td>
<td>Positive Charge-pump Delay Setting Pin</td>
</tr>
<tr>
<td>17</td>
<td>CPPFB</td>
<td>Positive Charge-pump Feedback Pin</td>
</tr>
<tr>
<td>18</td>
<td>CPGND</td>
<td>Charge-pump GND Pin</td>
</tr>
<tr>
<td>19</td>
<td>CPN</td>
<td>Negative Charge-pump Driver Output Pin</td>
</tr>
<tr>
<td>20</td>
<td>CPVCC</td>
<td>Power Pin for Charge-pump</td>
</tr>
<tr>
<td>21</td>
<td>CPP</td>
<td>Positive Charge-pump Driver Output Pin</td>
</tr>
<tr>
<td>22</td>
<td>CPPSW</td>
<td>Output Control Pin for Positive Charge-pump</td>
</tr>
<tr>
<td>23</td>
<td>LX</td>
<td>Step-up DC/DC Driver Output Pin</td>
</tr>
<tr>
<td>24</td>
<td>LX</td>
<td>Step-up DC/DC Driver Output Pin</td>
</tr>
</tbody>
</table>

* The tab on the bottom of the package enhances thermal performance and is electrically connected to GND (substrate level). It is recommended that the tab be connected to the ground plane on the board, or otherwise be left floating.
# ABSOLUTE MAXIMUM RATINGS

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Item</th>
<th>Ratings</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>VIN</td>
<td>VIN pin voltage</td>
<td>6.5</td>
<td>V</td>
</tr>
<tr>
<td>VDTC</td>
<td>DTC pin voltage</td>
<td>−0.3 to VIN + 0.3</td>
<td>V</td>
</tr>
<tr>
<td>VFB</td>
<td>VFB pin voltage</td>
<td>−0.3 to VIN + 0.3</td>
<td>V</td>
</tr>
<tr>
<td>VSS</td>
<td>SS pin voltage</td>
<td>−0.3 to VIN + 0.3</td>
<td>V</td>
</tr>
<tr>
<td>VDELAY</td>
<td>DELAY pin voltage</td>
<td>−0.3 to VIN + 0.3</td>
<td>V</td>
</tr>
<tr>
<td>VAMP</td>
<td>AMPOUT pin voltage</td>
<td>−0.3 to VIN + 0.3</td>
<td>V</td>
</tr>
<tr>
<td>VLX</td>
<td>LX pin voltage</td>
<td>−0.3 to 24</td>
<td>V</td>
</tr>
<tr>
<td>ILX</td>
<td>LX pin current</td>
<td>Internally limited</td>
<td>A</td>
</tr>
<tr>
<td>VREF</td>
<td>VREF pin voltage</td>
<td>−0.3 to VIN + 0.3</td>
<td>V</td>
</tr>
<tr>
<td>VCPVCC</td>
<td>CPVCC pin voltage</td>
<td>−0.3 to 24</td>
<td>V</td>
</tr>
<tr>
<td>VCE</td>
<td>CE pin voltage</td>
<td>−0.3 to VIN + 0.3</td>
<td>V</td>
</tr>
<tr>
<td>VRT</td>
<td>RT pin voltage</td>
<td>−0.3 to VIN + 0.3</td>
<td>V</td>
</tr>
<tr>
<td>VCPPDLY</td>
<td>CPPDLY pin voltage</td>
<td>−0.3 to VIN + 0.3</td>
<td>V</td>
</tr>
<tr>
<td>VCPNDLY</td>
<td>CPNDLY pin voltage</td>
<td>−0.3 to VIN + 0.3</td>
<td>V</td>
</tr>
<tr>
<td>VPFB</td>
<td>CPPFB pin voltage</td>
<td>−0.3 to VIN + 0.3</td>
<td>V</td>
</tr>
<tr>
<td>VNFB</td>
<td>CPNF pin voltage</td>
<td>−0.3 to VIN + 0.3</td>
<td>V</td>
</tr>
<tr>
<td>VCPP</td>
<td>CPP pin voltage</td>
<td>−0.3 to 24</td>
<td>V</td>
</tr>
<tr>
<td>VCPN</td>
<td>CPN pin voltage</td>
<td>−0.3 to 24</td>
<td>V</td>
</tr>
<tr>
<td>VPSW</td>
<td>CPPSW pin voltage</td>
<td>−0.3 to 24</td>
<td>V</td>
</tr>
<tr>
<td>IPSW</td>
<td>CPPSW pin current-A</td>
<td>20</td>
<td>mA</td>
</tr>
<tr>
<td>PD</td>
<td>Power dissipation (1)</td>
<td>4300</td>
<td>mW</td>
</tr>
<tr>
<td>Tj</td>
<td>Junction Temperature</td>
<td>−40 to 150</td>
<td>°C</td>
</tr>
<tr>
<td>Tstg</td>
<td>Storage temperature range</td>
<td>−55 to 150</td>
<td>°C</td>
</tr>
</tbody>
</table>

## ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the lifetime and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings are not assured.

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(1) Refer to **POWER DISSIPATION** in the APPENDIX for detailed information.
## RECOMMENDED OPERATING CONDITIONS

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Item</th>
<th>Rating</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$V_{\text{IN}}$</td>
<td>Input voltage</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>R1294L101A</td>
<td>2.0 to 5.5</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>R1294L102A</td>
<td>2.5 to 5.5</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>R1294L103A</td>
<td>3.3 to 5.5</td>
<td>V</td>
</tr>
<tr>
<td>CPVCC</td>
<td>CPVCC operating voltage</td>
<td>6 to 20</td>
<td>V</td>
</tr>
<tr>
<td>$T_a$</td>
<td>Operating temperature</td>
<td>−40 to 105</td>
<td>°C</td>
</tr>
</tbody>
</table>

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.
### ELECTRICAL CHARACTERISTICS

$V_{IN}$ is set as shown below for every version, unless otherwise noted.
- R1294L101A: $V_{IN} = 2.5$ V
- R1294L102A: $V_{IN} = 2.5$ V
- R1294L103A: $V_{IN} = 3.5$ V

The specifications surrounded by $\text{---}$ are guaranteed by design engineering at $-40^\circ C \leq T_a \leq 105^\circ C$.

#### R1294LxxA ($T_a = 25^\circ C$)

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Item</th>
<th>Conditions</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>$I_{IN}$</td>
<td>$V_{IN}$ Supply Current</td>
<td>$V_{IN} = 5.5$ V, $R_T = 24$ k$\Omega$</td>
<td>3.5</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td>$V_{UVLO1}$</td>
<td>UVLO Detect Voltage</td>
<td>R1294L101A</td>
<td>1.69</td>
<td>1.8</td>
<td>1.91</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>R1294L102A</td>
<td>2.05</td>
<td>2.2</td>
<td>2.35</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>R1294L103A</td>
<td>2.6</td>
<td>2.8</td>
<td>3.0</td>
<td>V</td>
</tr>
<tr>
<td>$V_{UVLO2}$</td>
<td>UVLO Release Voltage</td>
<td>R1294L101A</td>
<td>$V_{UVLO1} + 0.09$</td>
<td>2.0</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>R1294L102A</td>
<td>$V_{UVLO1} + 0.15$</td>
<td>2.5</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>R1294L103A</td>
<td>$V_{UVLO1} + 0.22$</td>
<td>3.2</td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$V_{FB}$</td>
<td>$V_{FB}$ Voltage</td>
<td></td>
<td>0.960</td>
<td>1.0</td>
<td>1.025</td>
<td>V</td>
</tr>
<tr>
<td>$V_{FBL}$</td>
<td>$V_{FB} \text{ &quot;L&quot; Output Voltage}$</td>
<td>$V_{FB} \times 0.85$</td>
<td></td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>$I_{FB}$</td>
<td>$V_{FB}$ Input Current</td>
<td>$V_{IN} = 5.5$ V, $V_{FB} = 0$ V or 5.5 V</td>
<td>-0.1</td>
<td></td>
<td>0.1</td>
<td>$\mu$A</td>
</tr>
<tr>
<td>$V_{DTC0}$</td>
<td>Duty = 0% DTC Voltage</td>
<td>$R_T = 24$ k$\Omega$</td>
<td>0.27</td>
<td>0.37</td>
<td>0.49</td>
<td>V</td>
</tr>
<tr>
<td>$V_{DTC20}$</td>
<td>Duty = 20% DTC Voltage</td>
<td>$R_T = 24$ k$\Omega$</td>
<td></td>
<td></td>
<td>0.49</td>
<td>V</td>
</tr>
<tr>
<td>$V_{DTC80}$</td>
<td>Duty = 80% DTC Voltage</td>
<td>$R_T = 24$ k$\Omega$</td>
<td></td>
<td></td>
<td>0.91</td>
<td>V</td>
</tr>
<tr>
<td>Maxduty</td>
<td>Maximum Duty Limit</td>
<td>$R_T = 24$ k$\Omega$, $V_{DTC} = V_{IN}$</td>
<td>84</td>
<td>91</td>
<td>98</td>
<td>%</td>
</tr>
<tr>
<td>$I_{AMPH}$</td>
<td>AMP &quot;H&quot; Output Current</td>
<td>$V_{FB} = 0.9$ V</td>
<td>R1294L101A/102A</td>
<td>1.4</td>
<td>3.2</td>
<td>5.8</td>
</tr>
<tr>
<td></td>
<td></td>
<td>R1294L103A</td>
<td>4.7</td>
<td></td>
<td>14.5</td>
<td>mA</td>
</tr>
<tr>
<td>$I_{AMPL}$</td>
<td>AMP &quot;L&quot; Output Current</td>
<td>$V_{FB} = 1.1$ V</td>
<td></td>
<td>30</td>
<td>80</td>
<td>135</td>
</tr>
<tr>
<td>$R_{ON}$</td>
<td>Switch ON Resistance</td>
<td></td>
<td>150</td>
<td></td>
<td></td>
<td>$\text{m}$Ω</td>
</tr>
<tr>
<td>$I_{LXOFF}$</td>
<td>Leakage Current</td>
<td>$V_{IN} = 5.5$ V, $V_{LX} = 20$ V</td>
<td>5</td>
<td></td>
<td></td>
<td>$\mu$A</td>
</tr>
<tr>
<td>$I_{LIMDC}$</td>
<td>Switch Limit Current</td>
<td></td>
<td>1.3</td>
<td></td>
<td></td>
<td>A</td>
</tr>
</tbody>
</table>
All test items listed under Electrical Characteristics are done under the pulse load condition (Tj = Ta = 25°C).

VIN is set as shown below for every version, unless otherwise noted.

- R1294L101A: VIN = 2.5 V
- R1294L102A: VIN = 2.5 V
- R1294L103A: VIN = 3.5 V

The specifications surrounded by [ ] are guaranteed by design engineering at −40°C ≤ Ta ≤ 105°C.

### R1294LxxA

(Ta = 25°C)

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Item</th>
<th>Conditions</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
<th>Unit</th>
</tr>
</thead>
<tbody>
<tr>
<td>fREQ</td>
<td>Oscillator Frequency</td>
<td>RT = 110 kΩ</td>
<td>165</td>
<td>210</td>
<td>280</td>
<td>kHz</td>
</tr>
<tr>
<td></td>
<td></td>
<td>RT = 24 kΩ</td>
<td>720</td>
<td>800</td>
<td>915</td>
<td>kHz</td>
</tr>
<tr>
<td></td>
<td></td>
<td>RT = 12 kΩ</td>
<td>1275</td>
<td>1400</td>
<td>1540</td>
<td>kHz</td>
</tr>
<tr>
<td>VREF</td>
<td>VREF Voltage</td>
<td>R1294L101A VIN = 2.0 to 5.5 V</td>
<td>1.18</td>
<td>1.2</td>
<td>1.23</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td></td>
<td>R1294L102A VIN = 2.5 to 5.5 V</td>
<td>5</td>
<td>15</td>
<td></td>
<td>mV</td>
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<tr>
<td></td>
<td></td>
<td>R1294L103A VIN = 3.3 to 5.5 V</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ΔVREF/ΔVIN</td>
<td>VREF Line Regulation</td>
<td>R1294L101A VIN = 2.0 to 5.5 V</td>
<td></td>
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<td>R1294L102A VIN = 2.5 to 5.5 V</td>
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<tr>
<td></td>
<td></td>
<td>R1294L103A VIN = 3.3 to 5.5 V</td>
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</tr>
<tr>
<td>ΔVREF/ΔIOUT</td>
<td>VREF Load Regulation</td>
<td>IOUT = 0.1 mA to 2.0 mA</td>
<td>6</td>
<td>25</td>
<td></td>
<td>mV</td>
</tr>
<tr>
<td>IOUT</td>
<td>VREF Current</td>
<td>R1294L101A VIN = 2.0 to 5.5 V</td>
<td>2.0</td>
<td></td>
<td></td>
<td>mA</td>
</tr>
<tr>
<td></td>
<td></td>
<td>R1294L102A VIN = 2.5 to 5.5 V</td>
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</tr>
<tr>
<td></td>
<td></td>
<td>R1294L103A VIN = 3.3 to 5.5 V</td>
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</tr>
<tr>
<td>ILIM</td>
<td>Short Current Limit</td>
<td>R1294L101A VIN = 2.0 to 5.5 V</td>
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<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>R1294L102A VIN = 2.5 to 5.5 V</td>
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<tr>
<td></td>
<td></td>
<td>R1294L103A VIN = 3.3 to 5.5 V</td>
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<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>ICVPCC</td>
<td>CPVCC Supply Current</td>
<td>CPVCC = 9 V, RT = 24 kΩ</td>
<td>500</td>
<td></td>
<td></td>
<td>µA</td>
</tr>
<tr>
<td>ISS</td>
<td>Soft-Start Current</td>
<td>CPVCC = 9 V</td>
<td>2.5</td>
<td>5.0</td>
<td>7.5</td>
<td>µA</td>
</tr>
<tr>
<td>IOS</td>
<td>CPP Soft-Start Time</td>
<td>CPVCC = 9 V</td>
<td>4.0</td>
<td></td>
<td></td>
<td>ms</td>
</tr>
<tr>
<td>IOSS</td>
<td>CPN Soft-Start Time</td>
<td>CPVCC = 9 V</td>
<td>4.0</td>
<td></td>
<td></td>
<td>ms</td>
</tr>
<tr>
<td>IPDLY</td>
<td>CPPDLY Charge Current</td>
<td>CPVCC = 9 V</td>
<td>2.5</td>
<td>5.0</td>
<td>7.5</td>
<td>µA</td>
</tr>
<tr>
<td>INDLY</td>
<td>CPNDLY Charge Current</td>
<td>CPVCC = 9 V</td>
<td>2.5</td>
<td>5.0</td>
<td>7.5</td>
<td>µA</td>
</tr>
<tr>
<td>VPDLY</td>
<td>CPPDLY Detector Threshold</td>
<td>CPVCC = 9 V</td>
<td>0.95</td>
<td>1.00</td>
<td>1.05</td>
<td>V</td>
</tr>
<tr>
<td>VNDLY</td>
<td>CPNDLY Detector Threshold</td>
<td>CPVCC = 9 V</td>
<td>0.95</td>
<td>1.00</td>
<td>1.05</td>
<td>V</td>
</tr>
<tr>
<td>VPFBL</td>
<td>CPPFB Fault Voltage</td>
<td>CPVCC = 9 V</td>
<td></td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>VPFB</td>
<td>CPPFB Voltage</td>
<td>CPVCC = 9 V</td>
<td>1.45</td>
<td>1.500</td>
<td>1.535</td>
<td>V</td>
</tr>
<tr>
<td>VNFBL</td>
<td>CPNFB Voltage</td>
<td>CPVCC = 9 V</td>
<td>−0.035</td>
<td>0.00</td>
<td>0.035</td>
<td>V</td>
</tr>
<tr>
<td></td>
<td>CPPFB Fault Voltage</td>
<td>CPVCC = 9 V</td>
<td>VPFBLx0.85</td>
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<td></td>
<td>V</td>
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</table>
VIN is set as shown below for every version, unless otherwise noted.
R1294L101A: VIN = 2.5 V
R1294L102A: VIN = 2.5 V
R1294L103A: VIN = 3.5 V

The specifications surrounded by are guaranteed by design engineering at −40°C ≤ Ta ≤ 105°C.

### R1294LxxA (Continued)  
(Ta = 25°C)

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Item</th>
<th>Conditions</th>
<th>Min.</th>
<th>Typ.</th>
<th>Max.</th>
<th>Unit</th>
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<tr>
<td>VNFBL</td>
<td>CPNFB Fault Voltage</td>
<td>CPVCC = 9 V</td>
<td>0.15</td>
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<td>RCPH</td>
<td>CPP &quot;H&quot; ON Resistance</td>
<td>CPVCC = 9 V</td>
<td>5</td>
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<td></td>
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<tr>
<td>RCPL</td>
<td>CPP &quot;L&quot; ON Resistance</td>
<td>CPVCC = 9 V</td>
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<td>Ω</td>
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<tr>
<td>RPCNH</td>
<td>CPN &quot;H&quot; ON Resistance</td>
<td>CPVCC = 9 V</td>
<td>5</td>
<td></td>
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<td>Ω</td>
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<tr>
<td>RPCNL</td>
<td>CPN &quot;L&quot; ON Resistance</td>
<td>CPVCC = 9 V</td>
<td>10</td>
<td></td>
<td></td>
<td>Ω</td>
</tr>
<tr>
<td>fREQCP</td>
<td>Charge-pump Frequency</td>
<td>CPVCC = 9 V</td>
<td></td>
<td></td>
<td>fREQ/4kHz</td>
<td>kHz</td>
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<tr>
<td>IDELAY1</td>
<td>DELAY Charge Current</td>
<td>CPVCC = 9 V</td>
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<td>5.0</td>
<td>7.5</td>
<td>µA</td>
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<tr>
<td>IDELAY2</td>
<td>DELAY Discharge Current</td>
<td>CPVCC = 9 V</td>
<td>200</td>
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<td>µA</td>
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<td>VDELAY</td>
<td>DELAY Detector Threshold</td>
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<td>1.0</td>
<td>1.06</td>
<td>V</td>
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<td>VPSW</td>
<td>CPPSW &quot;L&quot; Output Voltage</td>
<td>CPVCC = 9 V, I = 1 mA</td>
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<td></td>
<td>V</td>
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<tr>
<td>Istandby1</td>
<td>Standby Current</td>
<td></td>
<td>0.1</td>
<td>5</td>
<td></td>
<td>µA</td>
</tr>
<tr>
<td>Istandby2</td>
<td>CPVCC Standby Current</td>
<td></td>
<td>0.1</td>
<td>5</td>
<td></td>
<td>µA</td>
</tr>
<tr>
<td>VCEL</td>
<td>CE &quot;L&quot; Input Voltage</td>
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<td>V</td>
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<tr>
<td>VCEH</td>
<td>CE &quot;H&quot; Input Voltage</td>
<td>VIN = 5.5 V</td>
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<td>1.5</td>
<td></td>
<td>V</td>
</tr>
</tbody>
</table>

All test items listed under Electrical Characteristics are done under the pulse load condition (Tj = Ta = 25°C).
THEORY OF OPERATION

Overcurrent Protection
R1294L monitors the Nch-switch current of the step-up DCDC converter and limits the current. If Nch-switch current reaches the current limit, the R1294L immediately turns off Nch-switch. Nch-switch turns on every internal cycle, and the R1294L monitors Nch-switch current and turns off Nch-switch if Nch-switch current reaches the current limit again. By repeating this operation, the R1294L protects itself from the overcurrent.

Under Voltage Lock Out (UVLO)
If VIN pin voltage becomes equal to or lower than UVLO detector threshold, the R1294L immediately disables all the switching outputs (Lx, CPP, and CPN) as well as discharges the external capacitors on DTC pin and SS pin down to 0 V immediately, and the system will be reset.

Operation and Output Current of Step-up DC/DC Converter

< Typical Circuit >

Discontinuous Mode
Continuous Mode
In PWM step-up DC/DC converter, there are two modes; the discontinuous mode and the continuous mode. These two modes depend upon the continuous characteristic of the inductor current.

While PWM step-up DC/DC converter is turned on, the voltage into the inductance L will be $V_{IN}$, and the additional current ($i1$) can be calculated by the next formula.

$$\Delta i1 = V_{IN} \times \frac{ton}{L}$$

In the circuit of the step-up DC/DC converter, during the off time of the switching, the power is supplied. In this case, the decrease of input current ($i2$) can be calculated by the next formula:

$$\Delta i2 = (V_{OUT} - V_{IN}) \times \frac{Tf}{L}$$

In the PWM switching method, the current of inductor becomes continuous when it is $Tf = toff$. The operating of DC/DC converter becomes continuous mode. In the continuous mode, the variance of the ratio of current is equal ($\Delta i1 = \Delta i2$), therefore the DUTY in the continuous mode is calculated by the next formula.

$$duty(\%) = \frac{ton}{ton + toff} = \frac{(V_{OUT} - V_{IN})}{V_{OUT}}$$

If the input power and the output power are equal, the mode becomes continuous when the $I_{OUT}$ value is larger than the next formula.

$$V_{IN}^2 \times \frac{ton}{(2 \times L \times V_{OUT})}$$

The average of the inductor current when $Tf = toff$ is calculated by the next formula.

$$i1 \ (Ave.) = V_{IN} \times \frac{ton}{(2 \times L)}$$

The peak current ($IL_{xmax}$) of the inductor in the continuous mode can be calculated by the next formula:

$$IL_{xmax} = I_{OUT} \times \frac{V_{OUT}}{V_{IN} + V_{IN} \times \frac{ton}{(2 \times L)}}$$

$$IL_{xmax} = I_{OUT} \times \frac{V_{OUT}}{V_{IN} + V_{IN} \times T \times \frac{(V_{OUT} - V_{IN})}{(2 \times L \times V_{OUT})}}$$

As stated above, the value of the peak current becomes larger than the $I_{OUT}$ value, therefore note that the ILxmax to determine the I/O condition and the components around the I/O.

The actual maximum output current is 50 to 80% of the above-mentioned. Especially, in case that the IL is large, or $V_{IN}$ is low, the loss of $V_{IN}$ will be the amount of the ON resistance of the switch. As for the $V_{OUT}$, it is necessary to consider the $V_F$ of the diode (approximately 0.3 V).

Note: The above-mentioned explanation is based on the calculations of the ideal case. The external components or the loss of LX switching are not included.
TIMING CHART

Overall Sequence Timing Chart

The timing chart above describes from the power on to the $V_{OUT1}$, $V_{OUT2}$, and $V_{OUT3}$ turn on and until they are stable.

By releasing from the standby mode, $V_{OUT1}$ begins the soft-start, and the output voltage rises gradually. After preset soft-start time passes, and the $V_{OUT1}$ reaches the preset output voltage, the charge to capacitors set to CPPDLY pin and CPNDLY pin will start. CPPDLY pin and CPNDLY pin voltage reach respectively to the CPPDLY detector threshold ($V_{PDLY}$) and CPNDLY detector threshold ($V_{NDLY}$), then the soft-start of the charge-pump will begin. The delay time for soft-start of charge pump ($t_{PDLY}$, $t_{NDLY}$) can be set respectively.

When each delay time has passed, the soft-start of the charge-pump begins. $V_{OUT2}$ and $V_{OUT3}$ gradually turn on, and when the soft-start time ends, $V_{OUT2}$ and $V_{OUT3}$ reach the preset output voltage.
V\textsubscript{OUT1} Soft-start Operation

The timing chart above describes from the CE signal turns on until the soft-start of V\textsubscript{OUT1} ends.

**(STEP1)**
SS voltage gradually increases with the internal IC's constant current and the external capacitor. During the soft-start time, the amplifier's reference input to the OP AMP becomes an equal voltage as SS, and it gradually increases. Since V\textsubscript{OUT} reaches to the input voltage just after the power on, the VFB voltage rises at the specific voltage determined by the resistance ratio of the input voltage and the feedback part. However, the switching does not begin since AMPOUT is "L".

**(STEP2)**
When the SS becomes the specified voltage determined with the resistance ratio of the input voltage and the feedback part, the switching begins. In this case, the amplifier reference rises as well as SS, therefore V\textsubscript{OUT} rises to balance the amplifier reference and VFB. The DUTY in this case is determined by the three inputs PWM comparator, among the AMPOUT and DTC, the lowest voltage is selected.

**(STEP3)**
When the SS becomes 1 V, the soft-start ends. After that, the amplifier reference becomes the constant voltage (= 1 V), and the operation changes to the normal switching. At this time, the voltage of the AMPOUT becomes constant. The AMPOUT value is determined by the I/O voltage and the output current.
During the soft-start period, the soft-start time needs to be set shorter than the timer latch delay time due to the charging of DELAY pin. When the preset soft-start time finishes, the charging of DELAY pin stops and discharges to the GND.
APPLICATION INFORMATION

TYPICAL APPLICATION

R1294L10xA  Typical Application 1
Recommended External Components

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>L</td>
<td>NR4018T100M (for 210 kHz) / NR4018T4R7M (for 700 kHz) / NR4018T2R2M (for 1.4 MHz), Taiyo Yuden CLF7045T-100M-D (for 210 kHz) / CLF6045NIT-2R2N-D (for 1.4 MHz), TDK</td>
</tr>
<tr>
<td>D1</td>
<td>CRS10130A, TOSHIBA</td>
</tr>
<tr>
<td>D2-D7</td>
<td>1SS374, TOSHIBA</td>
</tr>
<tr>
<td>Tr1</td>
<td>2SA1586, TOSHIBA</td>
</tr>
</tbody>
</table>
Precautions for Selecting External Components

How to Set the Step-up Converter Output Voltage

\( V_{OUT1} \) of the step-up converter controls the voltage of \( V_{FB} \) pin, which should be \( V_{FB} = 1.0 \) V. It is possible to set \( V_{OUT1} \) voltage according to the following formula of \( R1 \) and \( R2 \) (refer to the Typical Application). \( V_{OUT1} \) voltage should be equal to or lower than 20 V. \( R1 + R2 \) should be equal to or lower than 500 kΩ.

\[
V_{OUT1} = V_{FB} \times \frac{R1 + R2}{R2}
\]

How to Set the Step-up Charge-pump Output Voltage

\( V_{OUT2} \) of the positive charge pump controls the voltage of \( C_{PPFB} \) pin, which should be \( V_{PPFB} = 1.5 \) V. It is possible to set \( V_{OUT2} \) voltage according to the following formula of \( R3 \) and \( R4 \) (refer to the Typical Application). \( R3 + R4 \) should be equal to or less than 500 kΩ.

\[
V_{OUT2} = V_{PPFB} \times \frac{R3 + R4}{R4}
\]

In the case of Typical Application 1, the maximum output voltage can be described as the following formula.

\[
V_{OUT2} \text{ (Max.)} = CP_{VCC} \times 2 - V_F \times 2 \quad \text{.. (VF is the forward voltage for the diodes D2-D3)}
\]

Set C15, D6 and D7 of diodes, and C16 (refer to the Typical Application 2) if the output voltage needs more than the range above. In this case, the maximum output voltage can be described as the following formula.

\[
V_{OUT2} \text{ (Max.)} = CP_{VCC} \times 3 - V_F \times 4 \quad \text{.. (VF is the forward voltage for diodes D2-D3, D6-D7)}
\]

The maximum load current of the boost charge pump is determined by \( C_{fly} \) (C13, C15), the oscillator frequency of charge pump (\( f_{REQCP} \)), and CPP “L” On Resistance (\( R_{CPPL} \)) as described in the following formula.

\[
I_{OUT2} \text{ (Max.)} = C_{fly} \times (1 - \exp (-1 / (2 \times C_{fly} \times R_{CPPL} \times f_{REQCP}))) \times (CP_{VCC} \times 2 - V_{OUT2} - V_F \times 2) \times f_{REQCP}
\]

How to Set the Inverting Charge-pump Output Voltage

\( V_{OUT3} \) of the inverting charge-pump controls the voltage of \( C_{NFB} \) pin, which should be \( V_{NFB} = 0 \) V. It is possible to set \( V_{OUT3} \) voltage by the following formula by \( R5 \) and \( R6 \) that are between \( V_{REF} \) pin and \( V_{OUT3} \) (refer to the Typical Application). \( R5 + R6 \) should be equal to or less than 500 kΩ.

\[
V_{OUT3} = V_{NFB} - (V_{REF} - V_{NFB}) \times R5 / R6
\]

The minimum output voltage can be set by the following formula.

\[
V_{OUT3} \text{ (Min.)} = -(CP_{VCC} - V_F \times 2) \quad \text{.. (VF is the forward voltage of the diode D4 and D5)}
\]

The maximum load current of inverting charge pump is determined by \( C_{fly} \) (C14), the oscillator frequency of charge pump (\( f_{REQCP} \)), and CPN “L” ON Resistance (\( R_{CPNL} \)) as described in the following formula.

\[
I_{OUT3} \text{ (Max.)} = C_{fly} \times (1 - \exp (-1 / (2 \times C_{fly} \times R_{CPNL} \times f_{REQCP}))) \times (CP_{VCC} + V_{OUT3} - V_F \times 2) \times f_{REQCP}
\]
How to set the Step-up DC/DC Converter's Phase Compensation (Refer to Typical Application)

In the DC/DC converter, with the load current and the external components (L and C) the phase may be delayed by 180 degrees. Due to this, the phase margin of system is lost and stability would be worse. Thus, it is necessary to proceed the phase, and keep a certain phase margin.

The phase compensation and the system gain can be set with using the resistor, R7 and capacitors, C7 and C8. The position and the setting values shown in the Typical Application are one of the examples.

Select R7 and C7, so that the cut-off frequency of this Zero point may become approximately the cutoff frequency of pole made by the external components (L and C). The following formula shows the pole made by the external components (L and C) and the “Zero” point.

\[
F_{\text{pole}} \approx \frac{1}{2 \pi \sqrt{L \times C_1}}
\]

\[
F_{\text{zero}} \approx \frac{1}{2 \pi R_7 \times C_7}
\]

For example, when \( L = 4.7 \, \mu\text{H} \) and \( C_\text{OUT} \, (C_1) = 20 \, \mu\text{F} \), the cut-off frequency of the pole is approximately 16 kHz. Then set the cut-off frequency of the Zero point around 16 kHz to 1.6 kHz.

The gain can be set with the ratio of the resistance of R7 and combined resistance of R1 and R2 \( (RT: RT = R_1 \times R_2/(R_1 + R_2)) \). If R7 is larger than the combined resistance (RT), the gain becomes high. If the gain is too high, the characteristics of response will be improved but the operating stability will be worse. Set R7 with an appropriate value.

Due to the R1 setting in the gain setting, another Zero point is set by R1 and C8. Set this cut-off frequency of Zero point at around the cut-off frequency by pole made by the external components (L and C). This Zero point is shown in the formula below.

\[
F_{\text{zero}} \approx \frac{1}{2 \pi R_1 \times C_8}
\]

Noise Reduction of the Feedback Voltage (Refer to Typical Application)

When the system noise is large, the output noise may be on to the feedback loop, and the operation may become unstable. In this case, set the value of the resistance R1 to R6 low and make the noise into the feedback reduction. It is possible to reduce the noise to the \( V_{FB} \) pin by connecting the resistance in the range from 1 k\( \Omega \) to 5 k\( \Omega \) around as R8.

Input Voltage

The range of \( V_\text{IN} \) voltage must be between 2.0 V and 5.5 V. For CPVCC pin, it is possible to use input \( V_{\text{OUT1}} \) or input another voltage of 6 V to 20 V to CPVCC as a power supply. In that case, set a capacitor of 1.0 \( \mu\text{F} \) or more as C16 between GND and CPVCC pin.
How to Set the Oscillator Frequency

Set a resistor (R12) between GND and RT pin. The oscillator frequency of the step-up converter (fREQ) can be set according to the next formula. This value depends upon the resistance value.

\[ f_{\text{REQ}} = 2.7 \times 10^{10} / \left[ 0.8542 \times R_{12} \times \left( 0.66 + \sqrt{0.66^2 + 12643 / R_{12}} \right) \right] \]

Set the frequency between 210 kHz and 1400 kHz. The oscillator frequency of the charge-pump is one fourth of the oscillator frequency of the main step-up DC/DC converter.

![Graph showing the relationship between Oscillating Frequency and RT Resistance](image)

How to Set the Soft-start of Step-up Converter (Refer to the Timing Chart)

The soft-start of the step-up converter operates when \( V_{IN} \) is equal to or more than the UVLO release voltage, or when CE signal is “H”. External capacitor of SS pin (C9) is charged with the soft-start charge current (\( I_{SS} \)). Then the voltage of SS pin is input to the error amplifier as the reference voltage. When the voltage of SS pin reaches to the reference voltage (Typ.1.0 V) in the normal state, the reference voltage of the error amplifier stabilized at 1.0 V, and it changes to the normal state. The soft-start of step-up converter time (\( t_{SS} \)) is set by the external capacitor (C9) for the SS pin in the next formula.

\[ t_{SS} = C_9 \times V_{FB} / I_{SS} \]

How to Set the Start-up Sequence (Refer to the Timing Chart)

When the output voltage of step-up converter is up to 85% of a set value, and the soft-start is finished, the external capacitors (C10 and C11) of the CPPDLY pin and the CPNDLY pin are charged by the CPPDLY charge current (\( I_{POLY} \)) and the CPNDLY charge current (\( I_{NDLY} \)). When the voltage of the CPPDLY pin and the CPNDLY pin charged up to the CPPDLY detector threshold (\( V_{PDLY} \)) and the CPNDLY detector threshold (\( V_{NDLY} \)), then the soft-start of the positive charge-pump and the negative charge-pump is operated respectively. After the step-up converter is operated, the delay time (\( t_{PDLY} \) and \( t_{NDLY} \)) until the soft-start of charge-pump is set by the external capacitors (C10 and C11) of the CPPDLY pin and the CPNDLY pin. The delay time is set by the following formula.

The delay time until the soft-start of positive charge-pump operates: \( t_{PDLY} = C_{10} \times V_{PDLY} / I_{POLY} \)

The delay time until the soft-start of negative charge-pump operates: \( t_{NDLY} = C_{11} \times V_{NDLY} / I_{NDLY} \)

Thus, after the main step-up DC/DC converter starts operating, the positive charge-pump and the negative charge-pump can be operated by the arbitrary order.
Soft-start of the Charge-pump (Refer to Typical Application and Timing Chart)

When the soft-start of boost charge-pump operates, the output of CPPSW changes from “H” to “L”. Setting the PNP-Tr1 (Tr1) keeps VOUT2 = 0 V until the positive charge-pump is started. If it is not required to keep VOUT2 = 0 V, then PNP-Tr1 is unnecessary. In this case, VOUT2 outputs approximately the same voltage as VOUT1. Arrange the resistor (R11) between the CPPSW pin and the base of PNP-Tr1 (Tr1). The maximum current of Tr1 can be set by the R11 value. This value can be calculated in the next formula.

\[ I_{\text{max}} = h_{\text{FE}} \times \frac{(V_{\text{OUT1}} - V_{\text{BE}})}{R_{11}} \cdots \]  

\[ h_{\text{FE}} \] is DC current gain of Tr1 and \[ V_{\text{BE}} \] is base emitter voltage of Tr1.

Select the appropriate value for R11 since the efficiency gets worse if the value is too small (refer to the Short Current Protection section. PNP-Tr1 has some effect on the operation of the short-current protection).

When the positive charge-pump starts, the reference voltage of the error amplifier starts from 0 V, turns on to the reference voltage (= 1.5 V) and becomes stable. Thus, the output voltage of VOUT2 can turn on by set output voltage within the time period of soft-start time.

In the initial state before starting the positive charge-pump, CPP pin generates High-level output voltage from the voltage supplied of CPVCC pin. Minim voltage of VOUT2 may occur when the “High” output voltage of CPP pin turns on by a rising of CPVCC voltage. The rising voltage level is susceptible to the rising width of CVPCCC voltage (CPVCC-VIN under the normal condition), the capacitor C13 for CPP pin, and the capacitor C2 for VOUT2. Since estimated calculation is \((\text{CPVCC}-\text{VIN})x\frac{C13}{C2+C13}\), maximum voltage is about 0.79V for \(\text{VIN}=3.3V, \text{CPVCC}=12V, C13=0.1\mu F, \text{and C2}=1\mu F\).

Before the soft-start of the negative charge-pump starts, the reference voltage of the error amplifier rises to VREF voltage (= 1.2 V) and falls down to 0 V in the soft start time fixed internally by the soft-start operation. Thus, the output voltage of VOUT3 can turn on by set output volatge within the time period of soft-start time.

How to set the Short Current Protection and Timer Latch Delay Time

If any output among the step-up converter output, the positive charge-pump output or the negative charge-pump output falls, the R1294L detects the short circuit. If this short circuit condition stays for a certain time, the latch-type protection circuit shuts down all the switching outputs (Lx, CPP, CPN) and outputs “H” through the CPPSW pin. Even if the switching stopped, the current path from CPVCC to VOUT2 is remained. If PNP-Tr is set on the CPPSW pin, the current path to VOUT2 is cut off after shutdown.

The detect voltages of VFB, CPPFB and CPNFB are:

- 85% of predetermined VFB voltage for VFB
- 85% of predetermined CPPFB voltage for CPPFB
- + 0.15 V for CPNFB

The latch timer delay is set by an external capacitor (C12) of the DELAY pin. This delay time can be calculated by the next formula.

\[ t_{\text{DLY}} = C12 \times \frac{V_{\text{DLY}}}{I_{\text{DLY}}} \]

To release the latch state, set VIN voltage below UVLO detector threshold and restart, or Set the CE pin "L" once and change the CE pin to "H" level.
How to set the Maxduty Limit

The value of maxduty can be set by the input voltage to DTC pin. Set the voltage in which the $V_{\text{REF}}$ output divided with the resistors R9 and R10. If the voltage of DTC pin increases more than the limit value, the lower value between the set value and the internally fixed value is selected and in valid.

TEST Pin

In terms of TEST pin, connect the GND level or remain it open.
Other Notes

• Use a 1.0 µF or higher capacitor (C4) in between GND and Vin pin. Connect the capacitor as close as possible to the IC. If the noise level is large, use the 4.7 µF or higher capacitor is recommended.

• Use a 1.0 µF or higher capacitor (C1, C2, and C3) in between GND and each VOUT (VOUT1, VOUT2, and VOUT3). The recommended capacitance is C1 = 4.7 µF to 22 µF, C2 = C3 = 1 µF to 2.2 µF (refer to the Typical Application).

• Use a 0.1 µF to 1 µF or higher capacitance (C6) in between VREF and GND.

• To connect the GND of the capacitors (C9, C10, C11, and C12) for setting the delay time as short as possible to the GND of the IC.

• Selection of the diodes and inductors and capacitors should be considered. When Nch-switch turns on, the high voltage of spike by an inductor might be generated. Thus, using more than twice of the set output voltage for the voltage tolerance of the capacitor connecting to VOUT is recommended. The diode and inductors should not exceed the rated value of the voltage, the current and the power.

• Select the diode with low forward voltage such as a Schottky barrier diode. The small reverse current and the fast switching speed type is desirable. Especially, the characteristics of diode (D1) influence the efficiency and the stability of the system.
TYPICAL CHARACTERISTICS

Typical Characteristics are intended to be used as reference data, they are not guaranteed.

1) V_{OUT1} (DCDC)

1-1) Output Voltage vs. Output Current

\( f_{REQ} = 210\,\text{kHz}, \, V_{OUT} = 8.0\,\text{V} \)

\( f_{REQ} = 700\,\text{kHz}, \, V_{OUT} = 8.0\,\text{V} \)

\( f_{REQ} = 1400\,\text{kHz}, \, V_{OUT} = 8.0\,\text{V} \)

\( f_{REQ} = 700\,\text{kHz}, \, V_{OUT} = 12.0\,\text{V} \)

\( f_{REQ} = 1400\,\text{kHz}, \, V_{OUT} = 12.0\,\text{V} \)

Typical Characteristics are intended to be used as reference data, they are not guaranteed.
1-2) Efficiency vs. Output Current

**f\textsubscript{REQ} = 210kHz, V\textsubscript{OUT} = 18.0V**

![Diagram showing Efficiency vs. Output Current for f\textsubscript{REQ} = 210kHz, V\textsubscript{OUT} = 18.0V]

**f\textsubscript{REQ} = 700kHz, V\textsubscript{OUT} = 18.0V**

![Diagram showing Efficiency vs. Output Current for f\textsubscript{REQ} = 700kHz, V\textsubscript{OUT} = 18.0V]

**f\textsubscript{REQ} = 1400kHz, V\textsubscript{OUT} = 18.0V**

![Diagram showing Efficiency vs. Output Current for f\textsubscript{REQ} = 1400kHz, V\textsubscript{OUT} = 18.0V]
f_{REQ} = 1400\,kHz, \quad V_{OUT} = 8.0\,V

f_{REQ} = 210\,kHz, \quad V_{OUT} = 12.0\,V

f_{REQ} = 700\,kHz, \quad V_{OUT} = 12.0\,V

f_{REQ} = 210\,kHz, \quad V_{OUT} = 18.0\,V

f_{REQ} = 700\,kHz, \quad V_{OUT} = 18.0\,V

\begin{align*}
V_{IN} &= 2.5\,V \\
V_{IN} &= 3.3\,V \\
V_{IN} &= 5.0\,V
\end{align*}
2) V_{OUT2} (Step-up Charge-pump)

2-1) Output Voltage vs. Output Current

- \( f_{REQ} = 1400\,kHz, \, V_{OUT} = 18.0\,V \)
- \( f_{REQ} = 700\,kHz, \, CPVCC = 8.0\,V, \, V_{OUT} = 12.0\,V \)
- \( f_{REQ} = 700\,kHz, \, CPVCC = 8.0\,V, \, V_{OUT} = 16.0\,V \)
- \( f_{REQ} = 1400\,kHz, \, CPVCC = 8.0\,V, \, V_{OUT} = 12.0\,V \)
- \( f_{REQ} = 1400\,kHz, \, CPVCC = 8.0\,V, \, V_{OUT} = 16.0\,V \)
3) $V_{OUT3}$ (Invert Charge-pump)

3-1) Output Voltage vs. Output Current

$f_{REQ} = 700\text{kHz}, \ CPVCC=8.0\text{V}, \ V_{OUT}=-6.0\text{V}$

$f_{REQ} = 1400\text{kHz}, \ CPVCC=8.0\text{V}, \ V_{OUT}=-6.0\text{V}$
**f\text{REQ} = 700\text{kHz}, CPVCC=12.0V, V\text{OUT}=-6.0V**

- C\text{fly}=0.022uF
- C\text{fly}=0.1uF

**f\text{REQ} = 1400\text{kHz}, CPVCC=12.0V, V\text{OUT}=-6.0V**

- C\text{fly}=0.022uF
- C\text{fly}=0.1uF

4) VFB Voltage vs. Input Voltage

5) Oscillator Frequency vs. Input Voltage
6) Supply Current vs. Input Voltage

- $f_{REQ} = 210\text{kHz}$, $Ta = 25^\circ\text{C}$
- $f_{REQ} = 700\text{kHz}$, $Ta = 25^\circ\text{C}$

7) Maxduty vs. Input Voltage

- $f_{REQ} = 1400\text{kHz}$, $Ta = 25^\circ\text{C}$
- $Ta = 25^\circ\text{C}$

8) VIN Supply Current vs. Temperature

9) CP Supply Current vs. Temperature
10) UVLO Detect Voltage vs. Temperature

![Graph of UVLO Detect Voltage vs. Temperature](image1)

11) UVLO Release Voltage vs. Temperature

![Graph of UVLO Release Voltage vs. Temperature](image2)
12) VFB Voltage vs. Temperature

![VFB Voltage vs. Temperature Graph](image)

13) Maxduty vs. Temperature

![Maxduty vs. Temperature Graph](image)

14) AMP "H" Output Current vs. Temperature

![AMP "H" Output Current vs. Temperature Graph](image)

15) AMP "L" Output Current vs. Temperature

![AMP "L" Output Current vs. Temperature Graph](image)

16) Switch ON Resistance vs. Temperature

![Switch ON Resistance vs. Temperature Graph](image)

17) Switch Leakage Current vs. Temperature

![Switch Leakage Current vs. Temperature Graph](image)
18) Switch Limit Current vs. Temperature

19) Oscillator Frequency vs. Temperature

20) VREF Voltage vs. Temperature

21) SS Pin Charge Current vs. Temperature
22) CPP Soft-start vs. Temperature

23) CPN Soft-start vs. Temperature

24) CPPDLY Charge Current vs. Temperature

25) CPNDLY Charge Current vs. Temperature

26) CPPDLY Detector Threshold vs. Temperature

27) CPNDLY Detector Threshold vs. Temperature
28) CPPFB Voltage vs. Temperature

29) CPNFB Voltage vs. Temperature

30) CPP "H" ON Resistance vs. Temperature

31) CPP "L" ON Resistance vs. Temperature

32) CPN "H" ON Resistance vs. Temperature

33) CPN "L" ON Resistance vs. Temperature
34) Charge-pump Frequency vs. Temperature

![Charge-pump Frequency Graph](image)

35) DELAY Charge Current vs. Temperature

![DELAY Charge Current Graph](image)

36) DELAY Discharge Current vs. Temperature

![DELAY Discharge Current Graph](image)

37) DELAY Detector Threshold vs. Temperature

![DELAY Detector Threshold Graph](image)

38) CPPSW "L" Output Voltage vs. Temperature

![CPPSW "L" Output Voltage Graph](image)
39) Standby Current vs. Temperature

40) CE "L" Input Voltage vs. Temperature

41) CE "H" Input Voltage vs. Temperature
42) Load Transient Response

**R1294L102A**

$V_{IN}=3.3\text{V}, V_{OUT}=8\text{V}, I_{OUT}=1\text{mA} - 100\text{mA}, f_{REQ}=210\text{kHz}$

![Graph 1](image1)

$V_{IN}=3.3\text{V}, V_{OUT}=12\text{V}, I_{OUT}=1\text{mA} - 100\text{mA}, f_{REQ}=210\text{kHz}$

![Graph 2](image2)

$V_{IN}=3.3\text{V}, V_{OUT}=16\text{V}, I_{OUT}=1\text{mA} - 100\text{mA}, f_{REQ}=210\text{kHz}$

![Graph 3](image3)
R1294L102A

$V_{IN}=3.3\,V$, $V_{OUT}=8\,V$, $I_{OUT}=1\,mA$ - $100\,mA$, f$_{REQ}=800kHz$

L 4.7uH
C1 20uF
R1 70kΩ
R2 10kΩ
C7 4700pF
R7 10kΩ
C8 100pF
R8 1kΩ

$V_{IN}=3.3\,V$, $V_{OUT}=12\,V$, $I_{OUT}=1\,mA$ - $100\,mA$, f$_{REQ}=800kHz$

L 4.7uH
C1 10uF
R1 110kΩ
R2 10kΩ
C7 4700pF
R7 10kΩ
C8 100pF
R8 1kΩ

$V_{IN}=3.3\,V$, $V_{OUT}=16\,V$, $I_{OUT}=1\,mA$ - $100\,mA$, f$_{REQ}=800kHz$

L 4.7uH
C1 10uF
R1 150kΩ
R2 10kΩ
C7 4700pF
R7 10kΩ
C8 100pF
R8 1kΩ
R1294L102A

$V_{IN}=3.3\text{V}$, $V_{OUT}=8\text{V}$, $I_{OUT}=1\text{mA} - 100\text{mA}$, $f_{REQ}=1400\text{kHz}$

![Graph 1](image1)

- L: 2.2$\mu$H
- C1: 20$\mu$F
- R1: 70k$\Omega$
- R2: 10k$\Omega$
- C7: 2200pF
- R7: 10k$\Omega$
- C8: 47pF
- R8: 1k$\Omega$

$V_{IN}=3.3\text{V}$, $V_{OUT}=12\text{V}$, $I_{OUT}=1\text{mA} - 100\text{mA}$, $f_{REQ}=1400\text{kHz}$

![Graph 2](image2)

- L: 2.2$\mu$H
- C1: 10$\mu$F
- R1: 110k$\Omega$
- R2: 10k$\Omega$
- C7: 2200pF
- R7: 10k$\Omega$
- C8: 47pF
- R8: 1k$\Omega$

$V_{IN}=3.3\text{V}$, $V_{OUT}=16\text{V}$, $I_{OUT}=1\text{mA} - 100\text{mA}$, $f_{REQ}=1400\text{kHz}$

![Graph 3](image3)

- L: 2.2$\mu$H
- C1: 10$\mu$F
- R1: 150k$\Omega$
- R2: 10k$\Omega$
- C7: 2200pF
- R7: 10k$\Omega$
- C8: 47pF
- R8: 1k$\Omega$
43) CE Switch Response

R1294L102A

CE Input Voltage

Output Voltage1

Output Voltage2

Output Voltage3

Time [ms]

Output Voltage [V]

CE Input Voltage

Output Voltage1

Output Voltage2

Output Voltage3

Time [ms]

Output Voltage [V]

CE Input Voltage

Output Voltage1

Output Voltage2

Output Voltage3

Time [ms]

Output Voltage [V]

CE Input Voltage

Output Voltage1

Output Voltage2

Output Voltage3

Time [ms]

Output Voltage [V]
The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following measurement conditions are based on JEDEC STD. 51-7.

### Measurement Conditions

<table>
<thead>
<tr>
<th>Item</th>
<th>Measurement Conditions</th>
</tr>
</thead>
<tbody>
<tr>
<td>Environment</td>
<td>Mounting on Board (Wind Velocity = 0 m/s)</td>
</tr>
<tr>
<td>Board Material</td>
<td>Glass Cloth Epoxy Plastic (Four-Layer Board)</td>
</tr>
<tr>
<td>Board Dimensions</td>
<td>76.2 mm × 114.3 mm × 0.8 mm</td>
</tr>
<tr>
<td>Copper Ratio</td>
<td>Outer Layer (First Layer): Less than 95% of 50 mm Square</td>
</tr>
<tr>
<td></td>
<td>Inner Layers (Second and Third Layers): Approx. 100% of 50 mm Square</td>
</tr>
<tr>
<td></td>
<td>Outer Layer (Fourth Layer): Approx. 100% of 50 mm Square</td>
</tr>
<tr>
<td>Through-holes</td>
<td>0.3 mm × 45 pcs</td>
</tr>
</tbody>
</table>

### Measurement Result

(Ta = 25°C, Tjmax = 150°C)

<table>
<thead>
<tr>
<th>Item</th>
<th>Measurement Result</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power Dissipation</td>
<td>4300 mW</td>
</tr>
<tr>
<td>Thermal Resistance (θja)</td>
<td>θja = 29°C/W</td>
</tr>
<tr>
<td>Thermal Characterization Parameter (ψjt)</td>
<td>ψjt = 10°C/W</td>
</tr>
</tbody>
</table>

θja: Junction-to-Ambient Thermal Resistance
ψjt: Junction-to-Top Thermal Characterization Parameter

![Power Dissipation vs. Ambient Temperature](image1.png)

![Measurement Board Pattern](image2.png)
The tab on the bottom of the package enhances thermal performance and is electrically connected to GND (substrate level). The tab is recommended to connect to the ground plane on the board. Otherwise it may be left floating.

HSOP-18 Package Dimensions
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