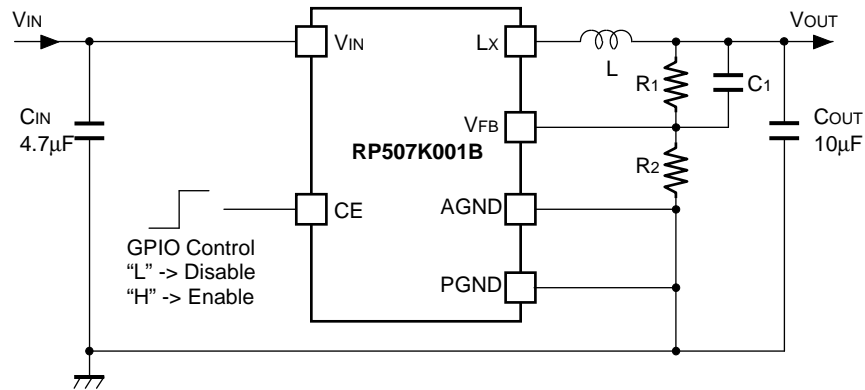
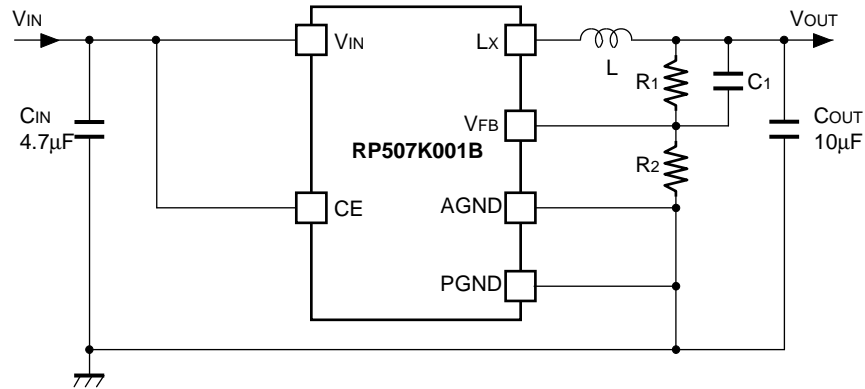
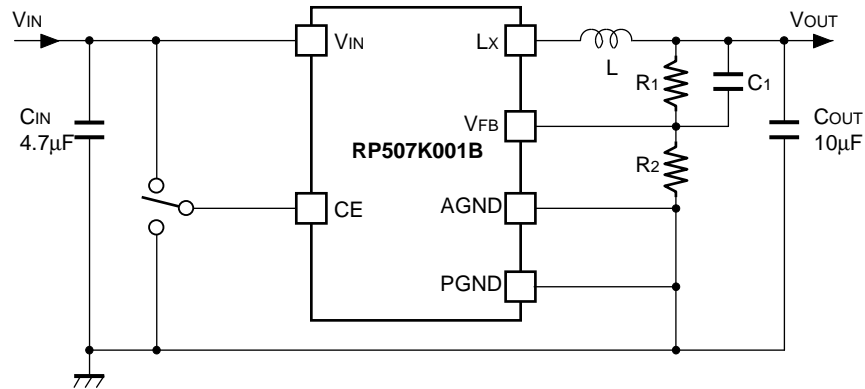


Design Guide

NO.ED-305-121112

■ TYPICAL APPLICATION

(Adjustable Output Voltage Type)



■ RECOMMENDED EXTERNAL COMPONENTS

Symbol	Value	Components	Part Number
C _{IN}	4.7μF	Ceramic Capacitor	C1005X5R0J475M (TDK) JMK105BBJ475MV (Taiyo Yuden) GRM155R60J475ME47 (Murata)
C _{OUT}	10μF	Ceramic Capacitor	GRM155R60J106ME44 (Murata) JMK105CBJ106MV (Taiyo Yuden)
L	2.2μH	Inductor	LQM21PN2R2NGC (Murata) CIG21L2R2MNE (Samsung Electro-Mechanics) MIPSZ2012D2R2 (FDK)
	4.7μH		CIG21L4R7MNE (Samsung Electro-Mechanics) MIPS2520D4R7 (FDK)

■ Set Output Voltage Range vs. Inductance Range

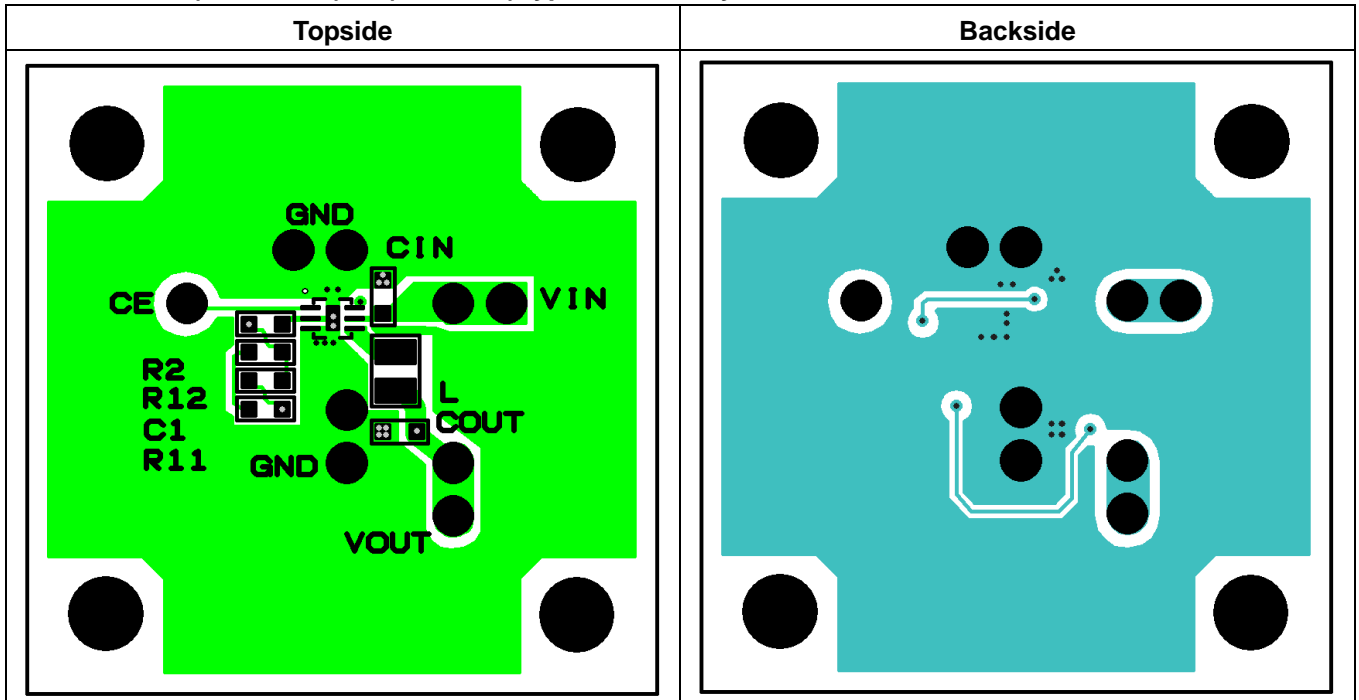
Set Output Voltage (V) V _{SET}	Inductance		
	L=1.5μH	L=2.2μH	L=4.7μH
0.7~1.0	Ok	Good	-
1.1~1.7	-	Good	-
1.8~2.5	-	Good	Ok
2.6~	-	Ok	Good

■ Set Output Voltage Range vs. Resistor & Capacitor Range

Set Output Voltage (V) V _{SET}	Resistor (kΩ)		Capacitor (pF)
	R1	R2	C1
1.0	120	180	22
1.2	180	180	22
1.5	270	180	22
1.8	240	120	22
2.5	380	120	15
2.8	275	75	15
3.3	270	60	15

■ TYPICAL BOARD LAYOUT

RP507K001B (PKG: DFN(PLP)1616-6D) typical board layout



*) R11 and R12 in R1 are arranged in series for series connection.



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