RICOH

R1180x SERIES

150mA LDO REGULATOR

NO.EA-105-111027

OUTLINE

The R1180x Series are CMOS-based voltage regulator ICs with high output voltage accuracy, extremely low supply current, and low ON-resistance. Each of these ICs consists of a voltage reference unit, an error amplifier, resistor-net for voltage setting, a current limit circuit which prevents the destruction by excess current, and so on. The output voltage of these ICs is fixed with high accuracy. B version has a chip enable pin, therefore ultra-low consumption current standby mode can be realized with the pin.

Since the packages for these ICs are SOT-23-5 (R1180N Series), SC-82AB (R1180Q Series), and SON1612-6 (R1180D Series), therefore high density mounting of the ICs on boards is possible.

FEATURES

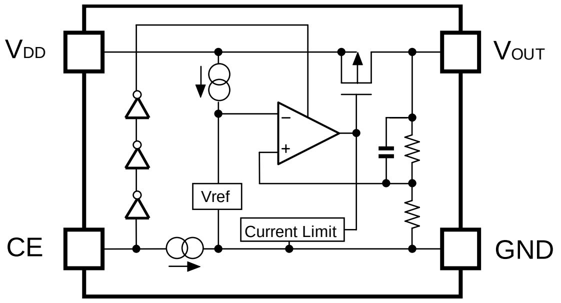
- Supply Current .... Typ. 1μA
  (Except the current through CE pull-down circuit)
- Standby Mode .... Typ. 0.1μA
- Dropout Voltage .... Typ. 0.25V (I_{OUT}=150mA 3.0V Output type)
- Temperature-Drift Coefficient of Output Voltage ...... Typ. ±100ppm/°C
- Line Regulation .... Typ. 0.05%/V
- Output Voltage Accuracy.. ±2.0%
- Packages SON1612-6, SC-82AB, SOT-23-5
- Output Voltage Range.. 1.2V to 3.6V (0.1V steps)
  (For other voltages, please refer to MARK INFORMATIONS.)
- Built-in Fold Back Protection Circuit .... Typ. 40mA (Current at short mode)
- Ceramic capacitors are recommended to be used with this IC 0.1μF

APPLICATIONS

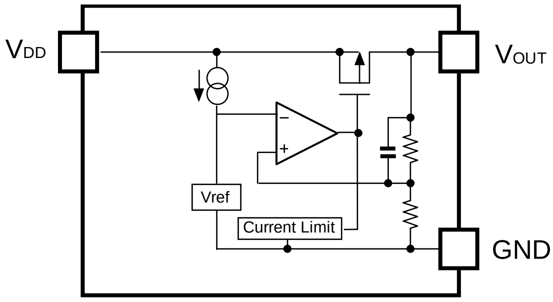
- Stable voltage reference.
- Power source for electrical appliances such as cameras, VCRs and camcorders.
- Power source for battery-powered equipment.

BLOCK DIAGRAMS

R1180xxx1B



R1180xxx1C



SELECTION GUIDE

The output voltage, CE pin polarity, package, etc. for the ICs can be selected at the user's request.

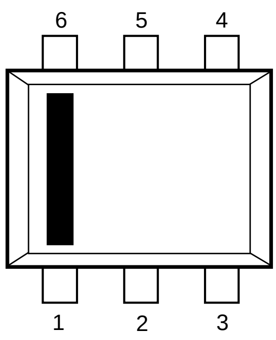
Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
R1180Dxx1∗-TR-FE	SON1612-6	4,000 pcs	Yes	Yes
R1180Qxx1∗-TR-FE	SC-82AB	3,000 pcs	Yes	Yes
R1180Nxx1∗-TR-FE	SOT-23-5	3,000 pcs	Yes	Yes

xx: The output voltage can be designated in the range from 1.2V(12) to 3.6V(36) in 0.1V steps.
(For other voltages, please refer to MARK INFORMATIONS.)

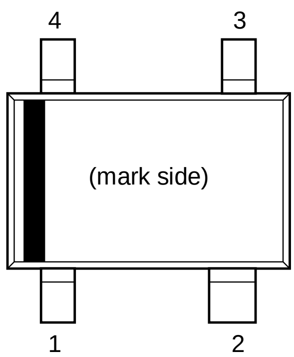
∗ : CE pin polarity are options as follows.
(B) "H" Active
(C) without CE pin

PIN CONFIGURATION

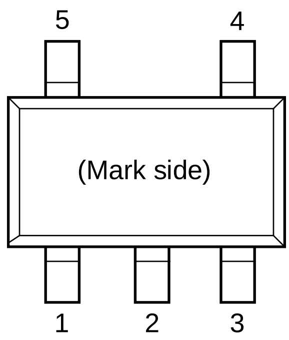
● **SON1612-6**



● **SC-82AB**



● **SOT-23-5**



PIN DESCRIPTIONS

● **SON1612-6**

Pin No	Symbol	Pin Description
1	V_{DD}	Input Pin
2	GND	Ground Pin
3	V_{OUT}	Output pin
4	NC	No Connection
5	GND	Ground Pin
6	CE or NC	Chip Enable Pin or No Connection

● **SC-82AB**

Pin No	Symbol	Pin Description
1	CE or NC	Chip Enable Pin or No Connection
2	GND	Ground Pin
3	V_{OUT}	Output pin
4	V_{DD}	Input Pin

● **SOT-23-5**

Pin No	Symbol	Pin Description
1	V_{DD}	Input Pin
2	GND	Ground Pin
3	CE or NC	Chip Enable Pin or No Connection
4	NC	No Connection
5	V_{OUT}	Output pin

ABSOLUTE MAXIMUM RATINGS

Symbol	Item	Rating	Unit
V_{IN}	Input Voltage	6.5	V
V_{CE}	Input Voltage (CE Pin)	6.5	V
V_{OUT}	Output Voltage	–0.3 to V_{IN}+0.3	V
I_{OUT}	Output Current	180	mA
P_D	Power Dissipation (SON1612-6)*	500	mW
	Power Dissipation (SC-82AB)*	380	
	Power Dissipation (SOT-23-5)*	420	
Topt	Operating Temperature Range	–40 to 85	°C
Tstg	Storage Temperature Range	–55 to 125	°C

∗) For Power Dissipation, please refer to PACKAGE INFORMATION.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field.
The functional operation at or over these absolute maximum ratings is not assured.

RECOMMENDED OPERATING CONDITIONS (ELECTRICAL CHARACTERISTICS)

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

ELECTRICAL CHARACTERISTICS

- **R1180xxx1B/C**

Topt=25°C

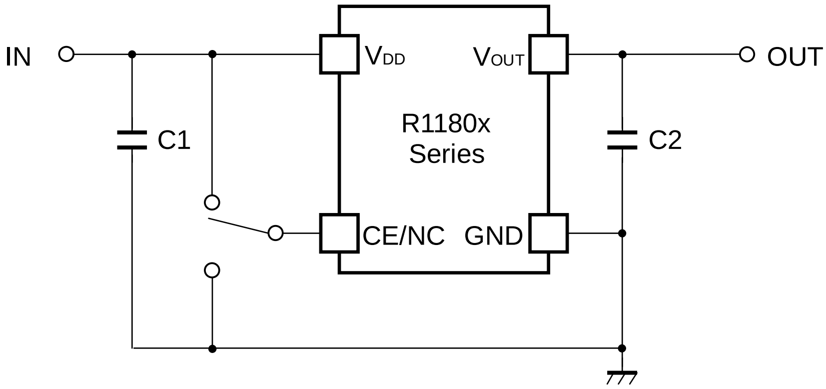
Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
V_{OUT}	Output Voltage	V_{IN}=Set V_{OUT}+1V 1μA ≤ I_{OUT} ≤ 30mA	×0.980		×1.020	V
I_{OUT}	Output Current	V_{IN}–V_{OUT}=1.0V(V_{OUT} ≥ 1.5V) V_{IN}=2.4V(V_{OUT}<1.5V)	150			mA
$\Delta V_{OUT}/\Delta I_{OUT}$	Load Regulation	V_{IN}–V_{OUT}=1.0V(V_{OUT} ≥ 1.5V) V_{IN}=2.4V(V_{OUT}<1.5V) 1μA ≤ I_{OUT} ≤ 150mA		20	40	mV
V_{DIF}	Dropout Voltage	Refer to the ELECTRICAL CHARACTERISTICS by OUTPUT VOLTAGE				
I_{SS}	Supply Current	V_{IN}–V_{OUT}=1.0V,I_{OUT}=0mA		1.0	1.5	μA
Istandby	Supply Current (Standby)	V_{IN}–V_{OUT}=1.0V,V_{CE}=GND		0.1	1.0	μA
$\Delta V_{OUT}/\Delta V_{IN}$	Line Regulation	I_{OUT}=30mA V_{OUT}+0.5V ≤ V_{IN} ≤ 6.0V (V_{OUT} ≥ 1.5V) 2.0V ≤ V_{IN} ≤ 6.0V (1.2V ≤ V_{OUT} ≤ 1.4V)		0.05	0.20	%/V
V_{IN}	Input Voltage		1.7		6.0	V
$\Delta V_{OUT}/\Delta T_{opt}$	Output Voltage Temperature Coefficient	I_{OUT}=30mA –40°C ≤ Topt ≤ 85°C		±100		ppm /°C
I_{SC}	Short Current Limit	V_{OUT}=0V		40		mA
I_{PD}	CE Pull-down Constant Current	(R1180xxx1B)		0.35		μA
V_{CEH}	CE Input Voltage "H"	(R1180xxx1B)	1.2		6.0	V
V_{CEL}	CE Input Voltage "L"	(R1180xxx1B)	0.0		0.3	V

- **ELECTRICAL CHARACTERISTICS by OUTPUT VOLTAGE**

Topt = 25°C

Output Voltage V_{OUT} (V)	Dropout Voltage V_{DIF} (V)		
	Condition	Typ.	Max.
1.2 ≦ V_{OUT} < 1.3	I_{OUT}=150mA	0.85	1.20
1.3 ≦ V_{OUT} < 1.4		0.75	1.10
1.4 ≦ V_{OUT} < 1.5		0.65	1.00
1.5 ≦ V_{OUT} < 1.7		0.60	0.90
1.7 ≦ V_{OUT} < 1.9		0.50	0.75
1.9 ≦ V_{OUT} < 2.1		0.40	0.65
2.1 ≦ V_{OUT} < 2.8		0.35	0.55
2.8 ≦ V_{OUT} ≦ 3.6		0.25	0.40

TYPICAL APPLICATION



(External Components)
Output Capacitor
Ceramic Capacitor 0.1μF

TECHNICAL NOTES

When using these ICs, consider the following points:

Phase Compensation

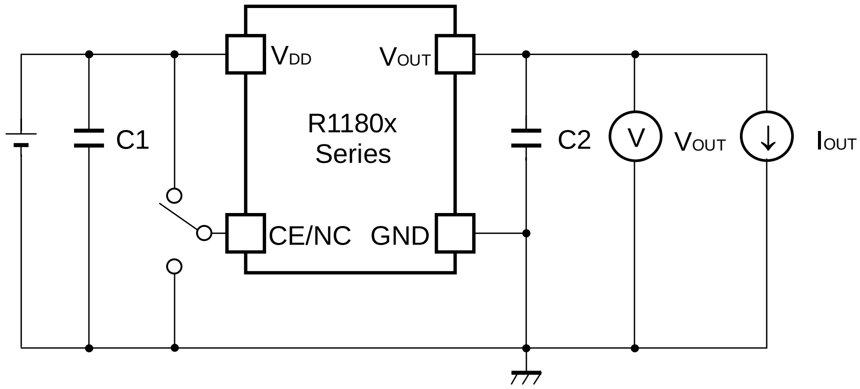
In these ICs, phase compensation is made for securing stable operation even if the load current is varied. For this purpose, use a capacitor C2 with good frequency characteristics and ESR (Equivalent Series Resistance). (Note: If additional ceramic capacitors are connected with parallel to the output pin with an output capacitor for phase compensation, the operation might be unstable. Because of this, test these ICs with as same external components as ones to be used on the PCB.)

PCB Layout

Make V_{DD} and GND lines sufficient. If their impedance is high, noise pickup or unstable operation may result. Connect a capacitor C1 with a capacitance value as much as 0.1μF or more between V_{DD} and GND pin, and as close as possible to the pins.

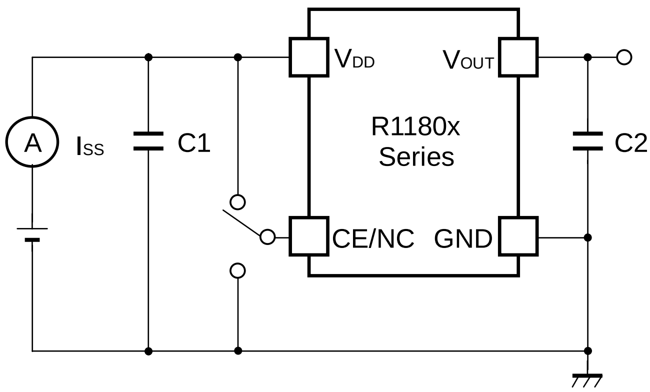
Set external components, especially the output capacitor C2, as close as possible to the ICs, and make wiring as short as possible.

TEST CIRCUITS



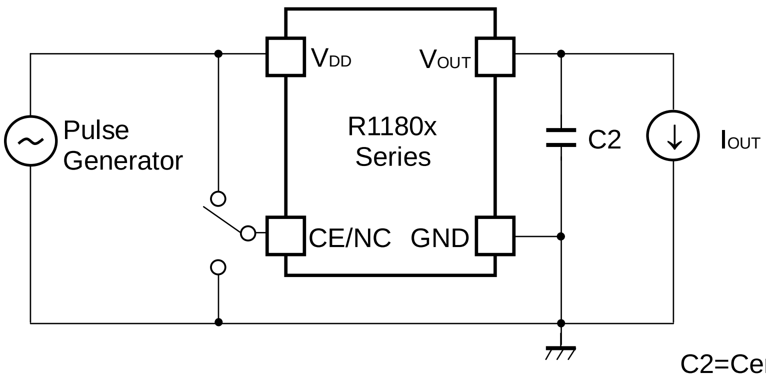
C1=Ceramic 1.0μF
C2=Ceramic 0.1μF

Standard test Circuit



C1=Ceramic 1.0μF
C2=Ceramic 0.1μF

Supply Current Test Circuit



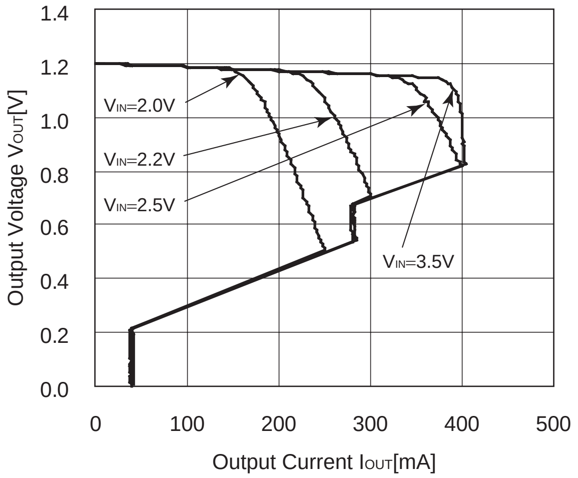
C2=Ceramic 0.1μF

Ripple Rejection, Line Transient Response Test Circuit

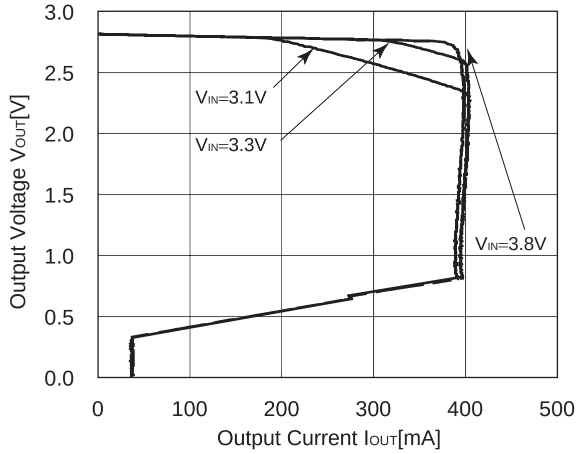
TYPICAL CHARACTERISTICS

1) Output Voltage vs. Output Current (Topt=25°C)

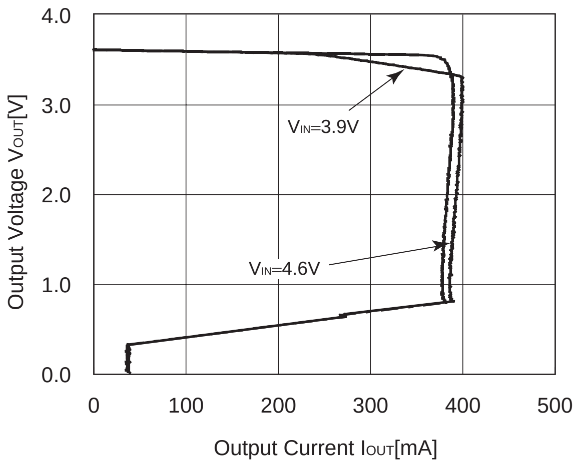
R1180x121x



R1180x281x

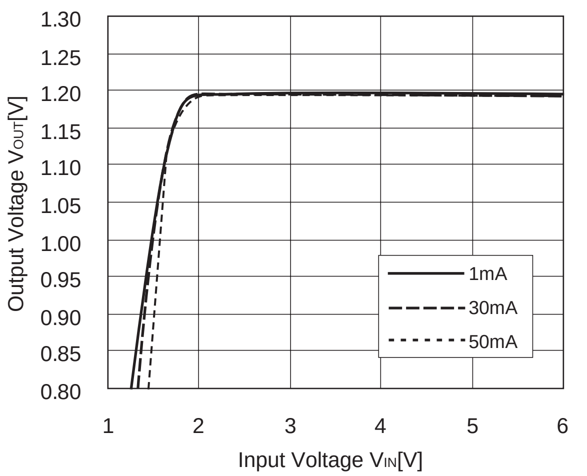


R1180x361x

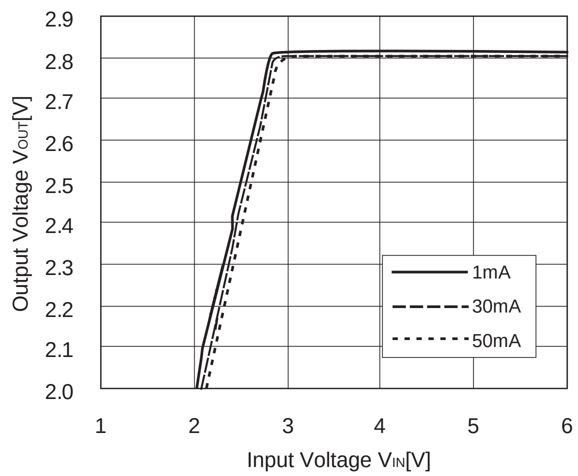


2) Output Voltage vs. Input Voltage (Topt=25°C)

R1180x121x



R1180x281x



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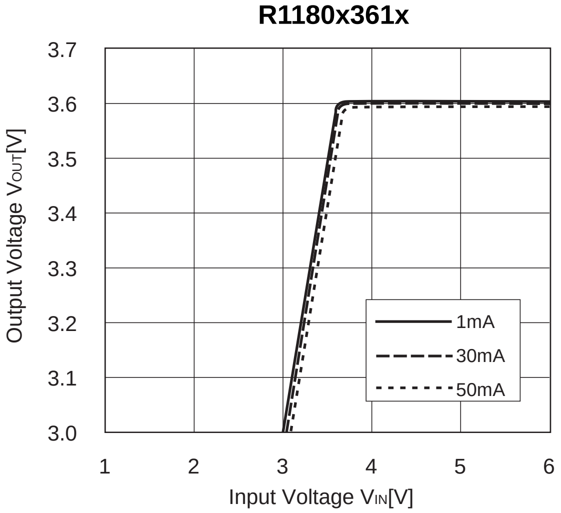
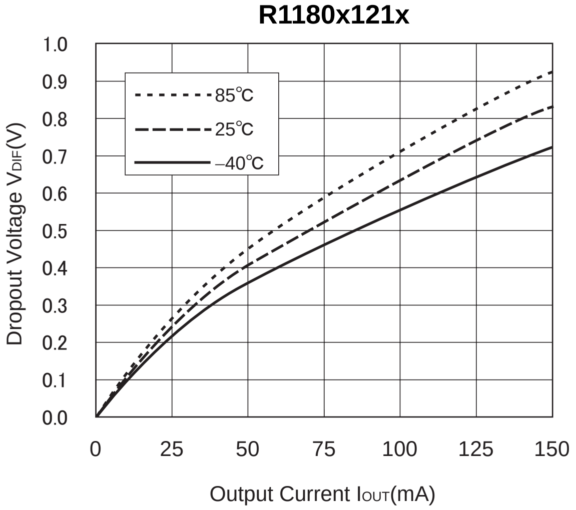
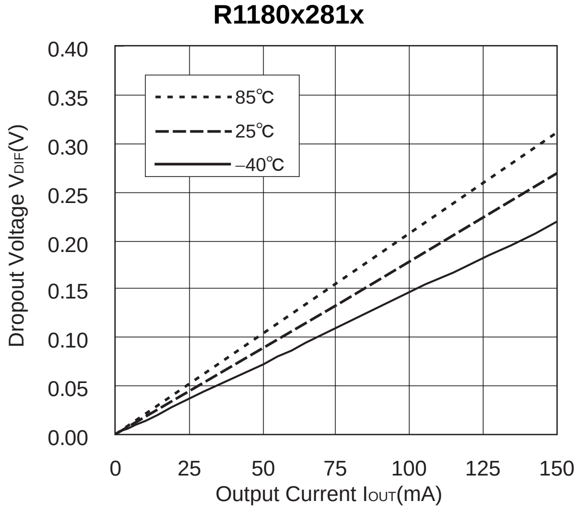
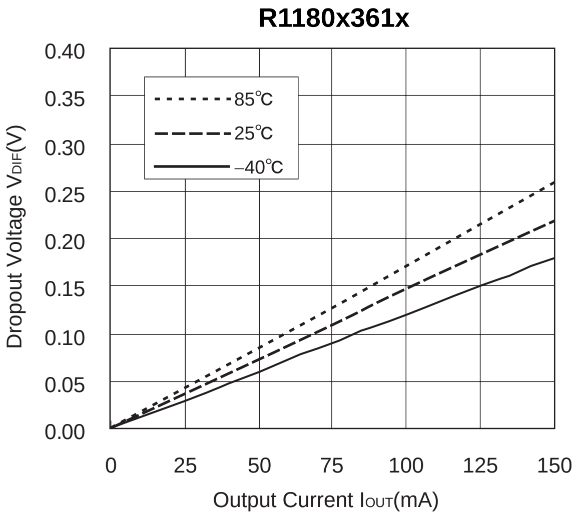


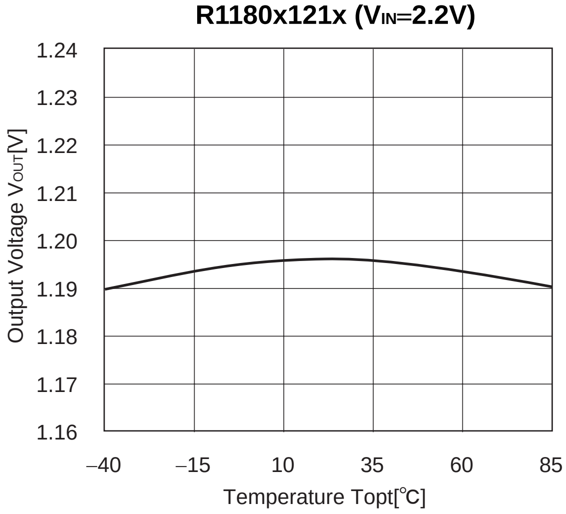
3) Dropout Voltage vs. Output Current

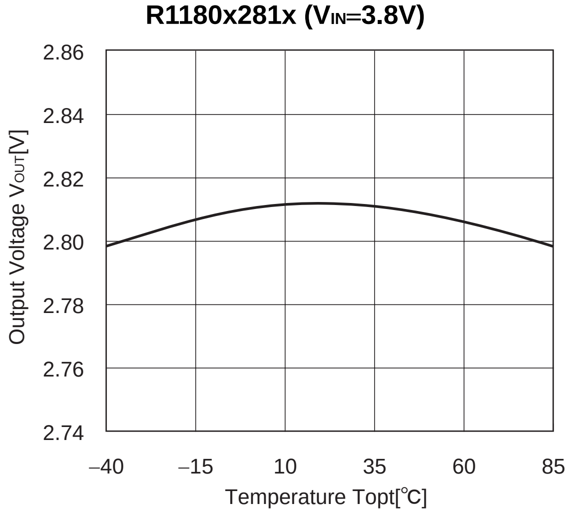


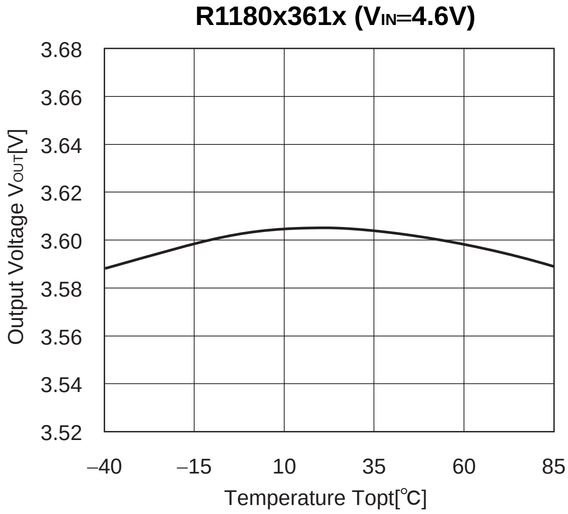




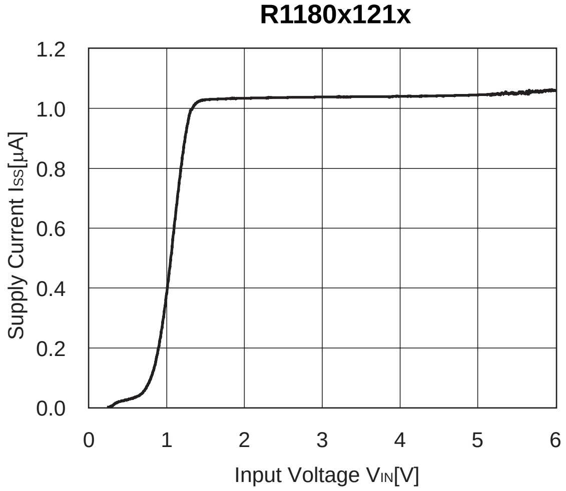
4) Output Voltage vs. Temperature (I_{OUT}=30mA)

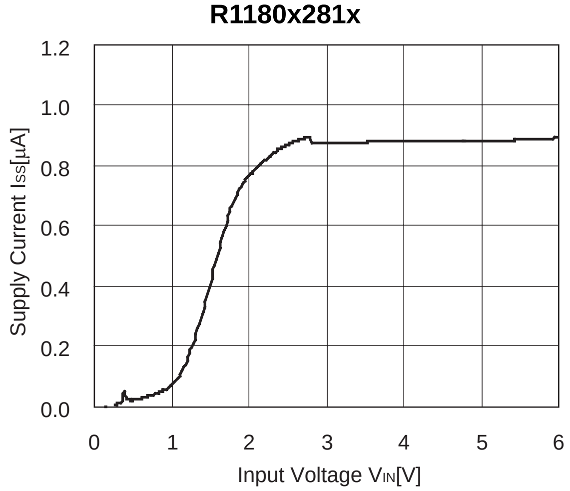






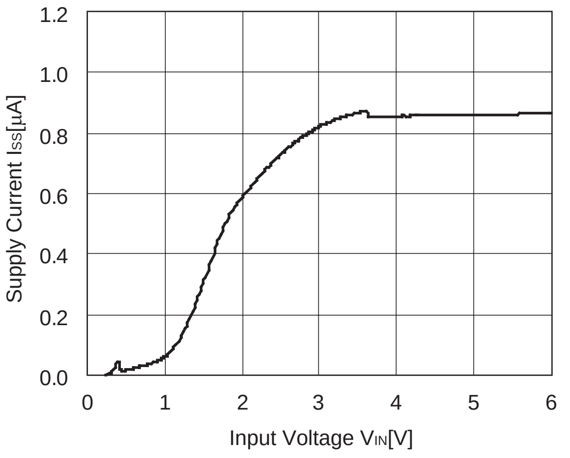
5) Supply Current vs. Input Voltage (Topt=25°C)





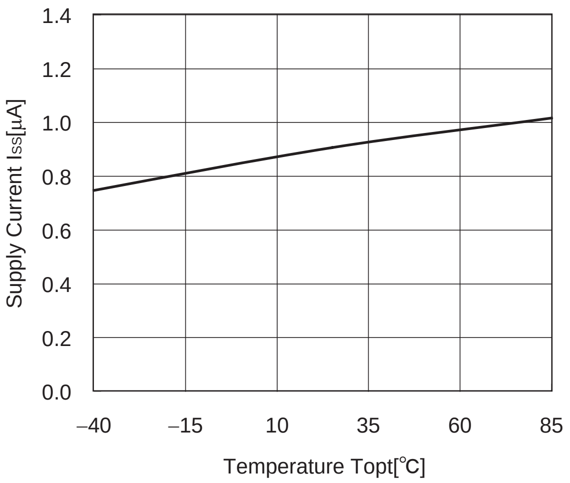
RICOH

R1180x361x

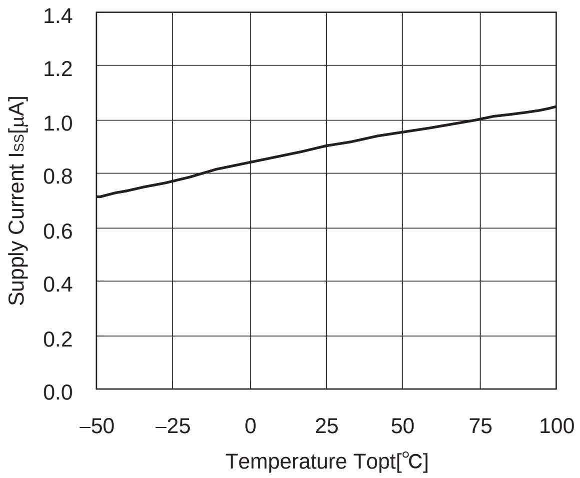


6) Supply Current vs. Temperature

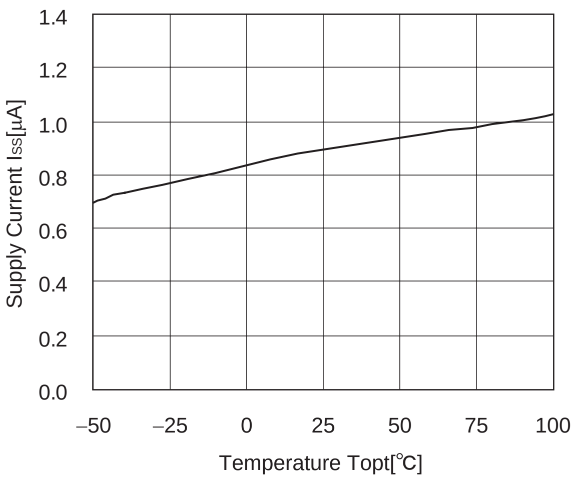
R1180x121x(VIN=2.2V)



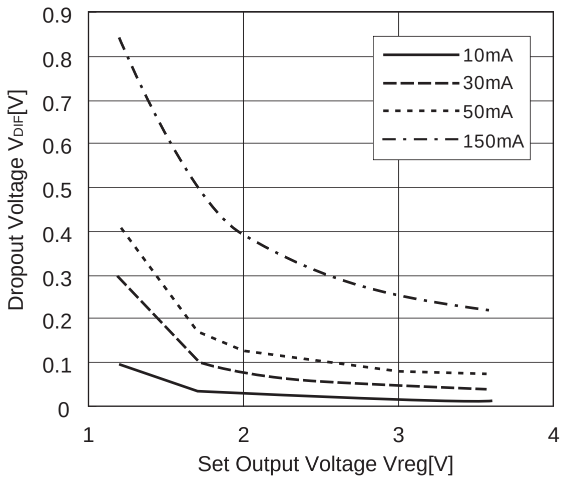
R1180x281x(VIN=3.8V)



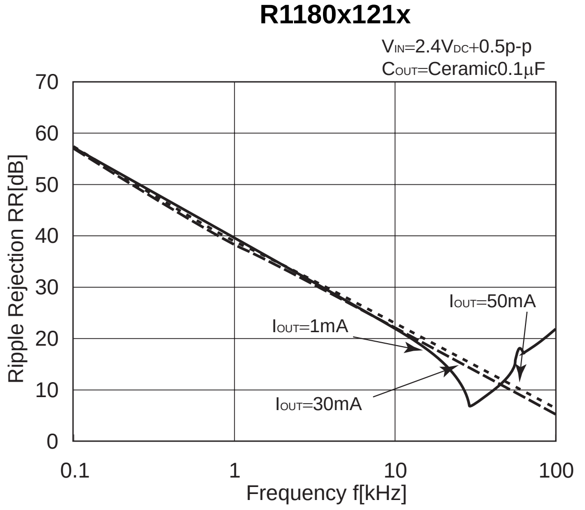
R1180x361x(VIN=4.6V)

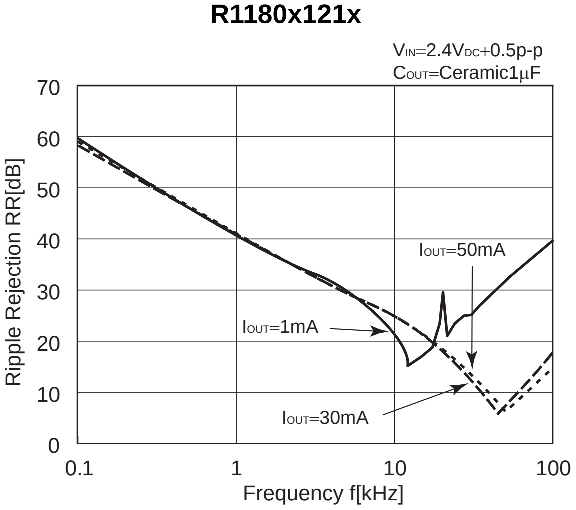


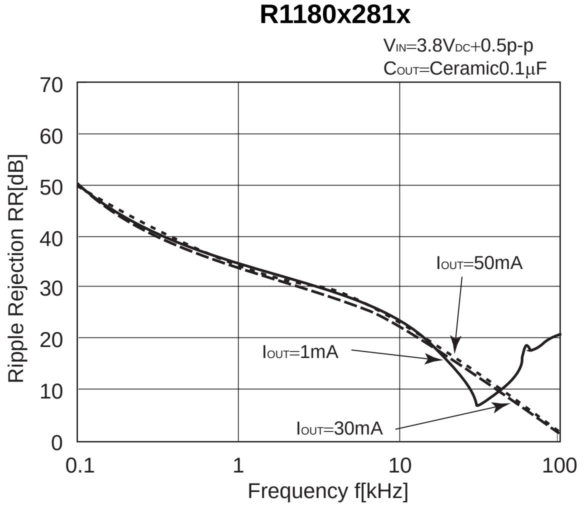
7) Dropout Voltage vs. Set Output Voltage (Topt=25°C)

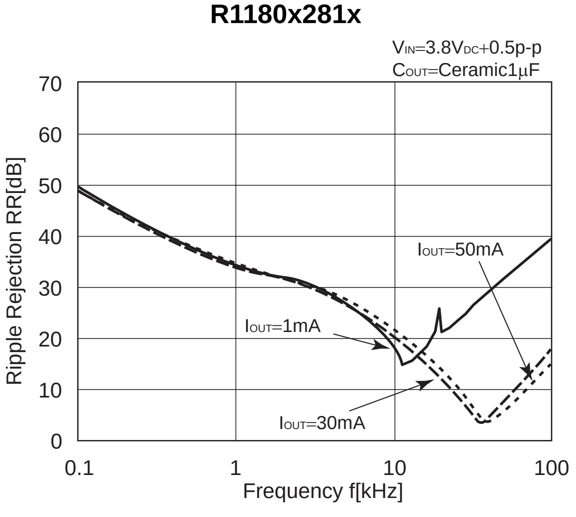


8) Ripple Rejection vs. Frequency (C_{IN}=none)

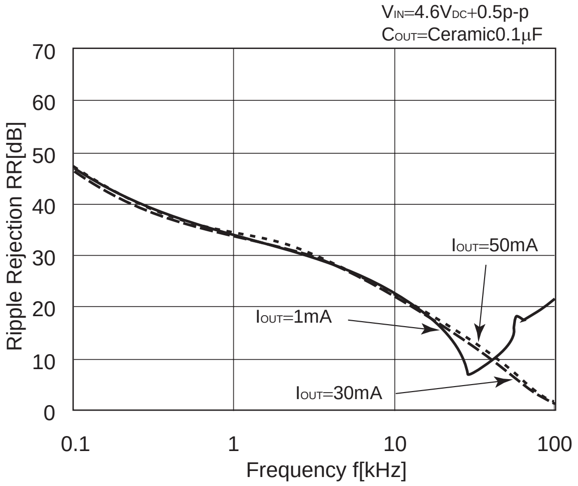




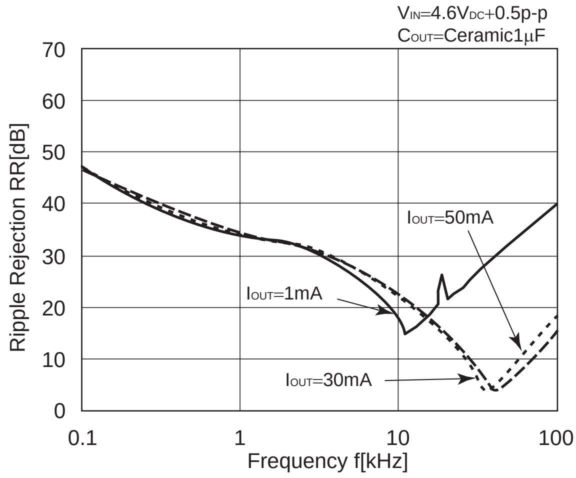




R1180x361x

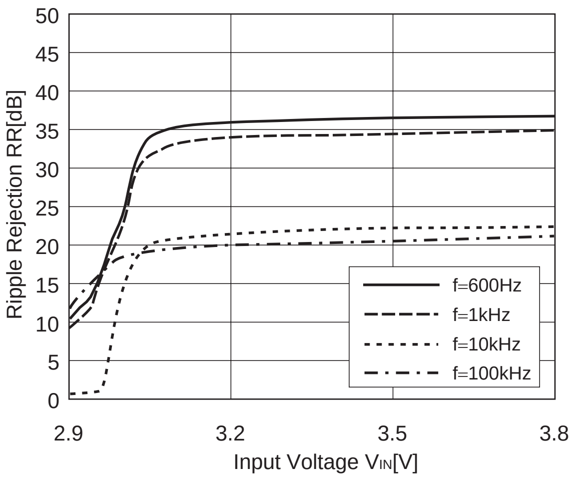


R1180x361x

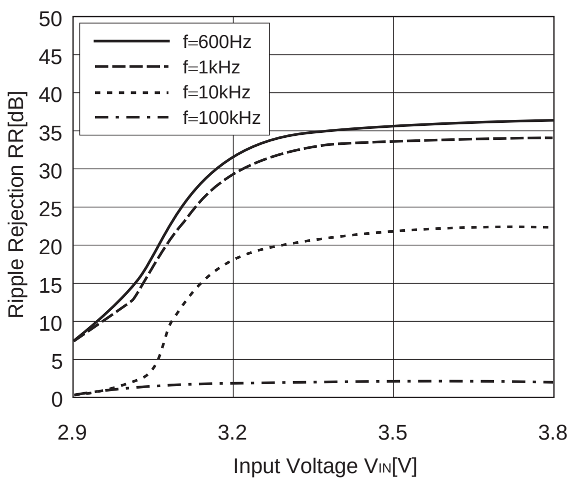


9) Ripple Rejection vs. Input Bias Voltage (Topt=25°C, C_{IN}=none, C_{OUT}=Ceramic0.1μF)

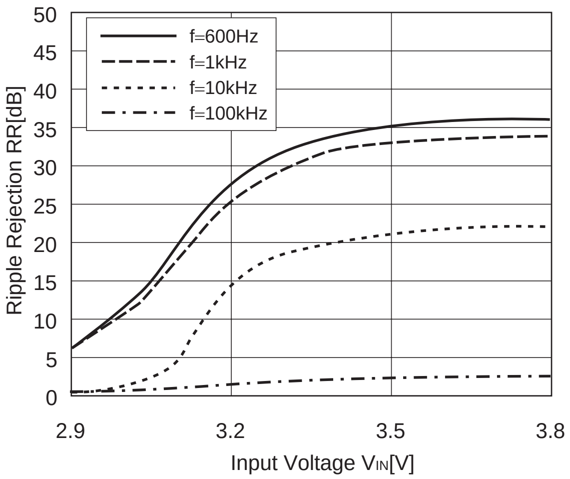
R1180x281x(I_{OUT}=1mA)



R1180x281x(I_{OUT}=30mA)

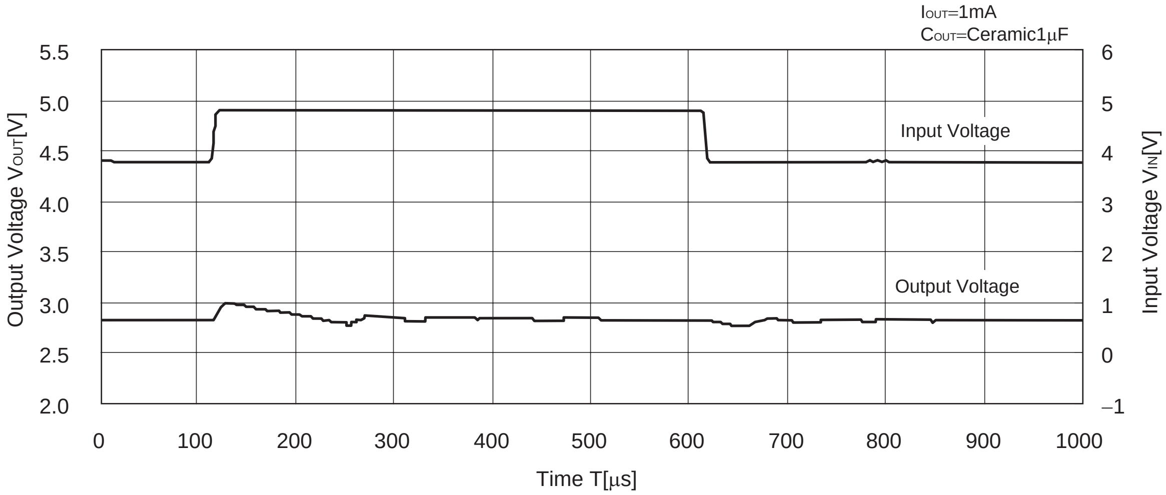


R1180x281x(I_{OUT}=50mA)

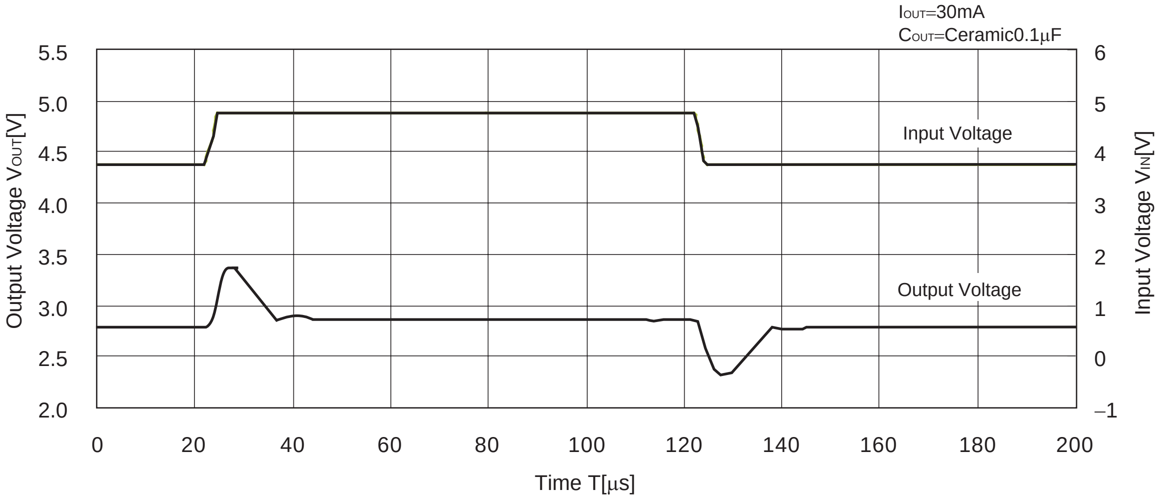


10) Input Transient Response (C_{IN}=none, tr=tf=5μs)

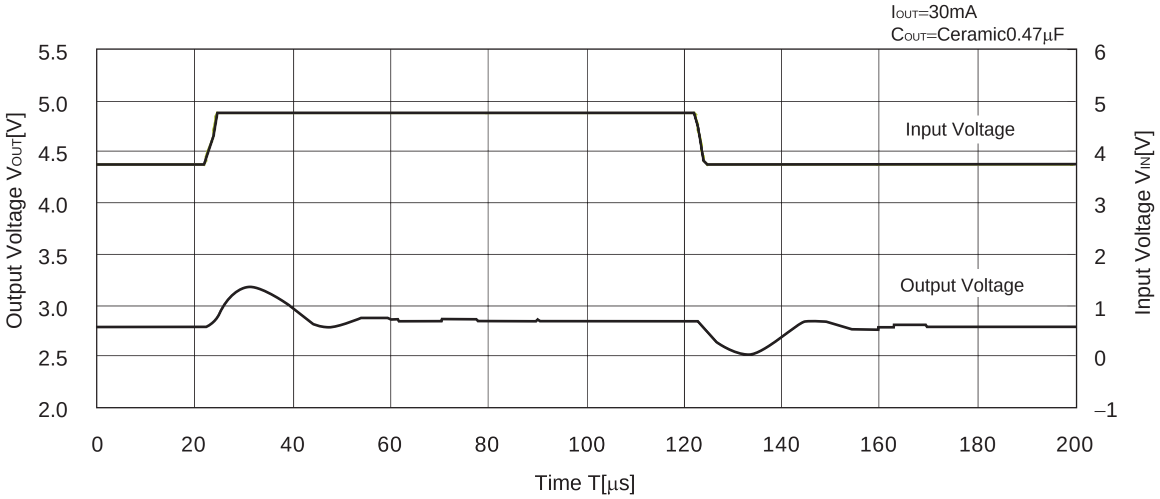
R1180x281x

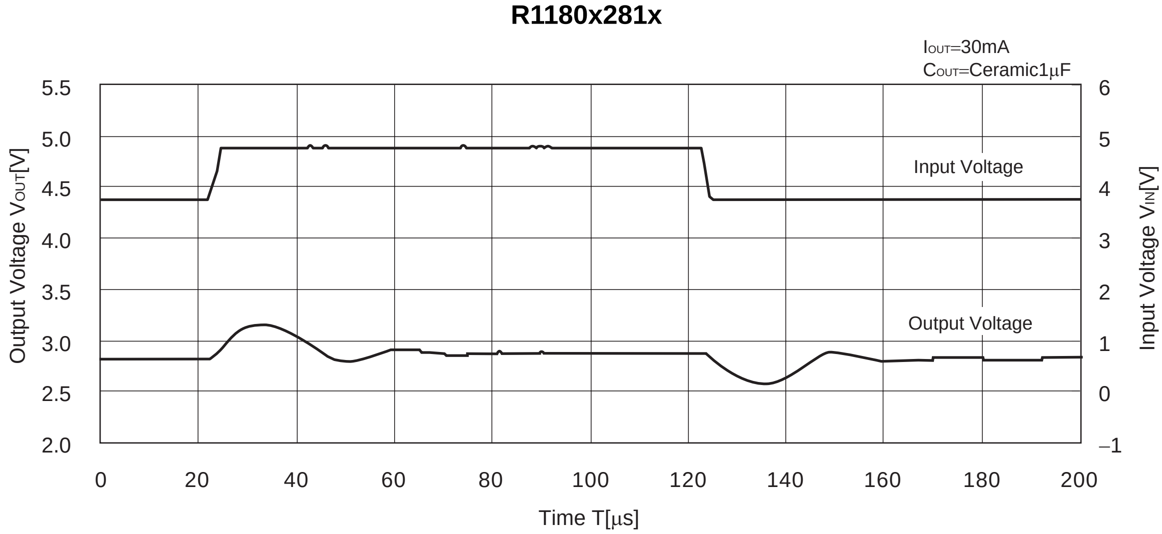


R1180x281x

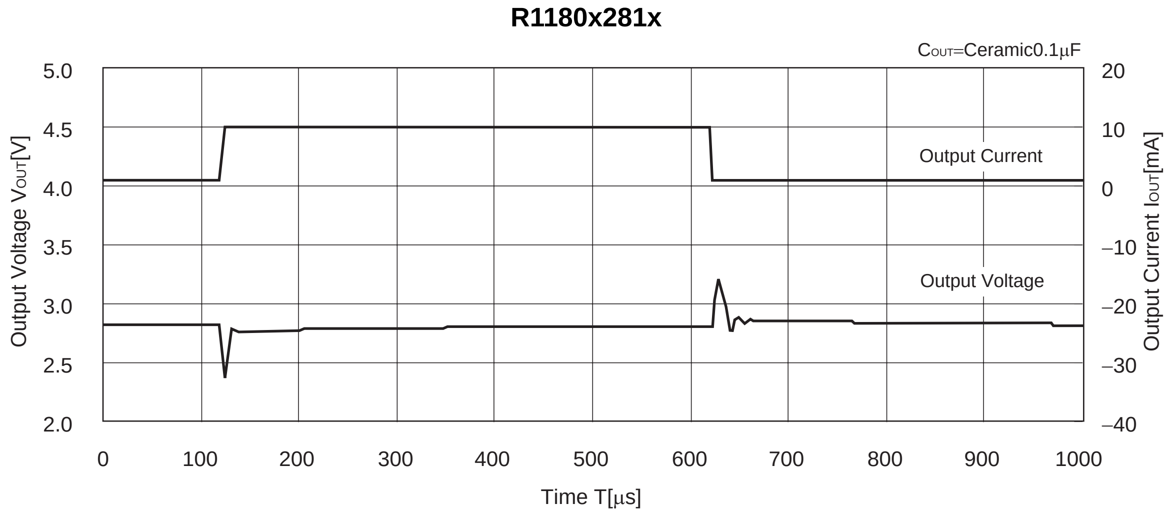


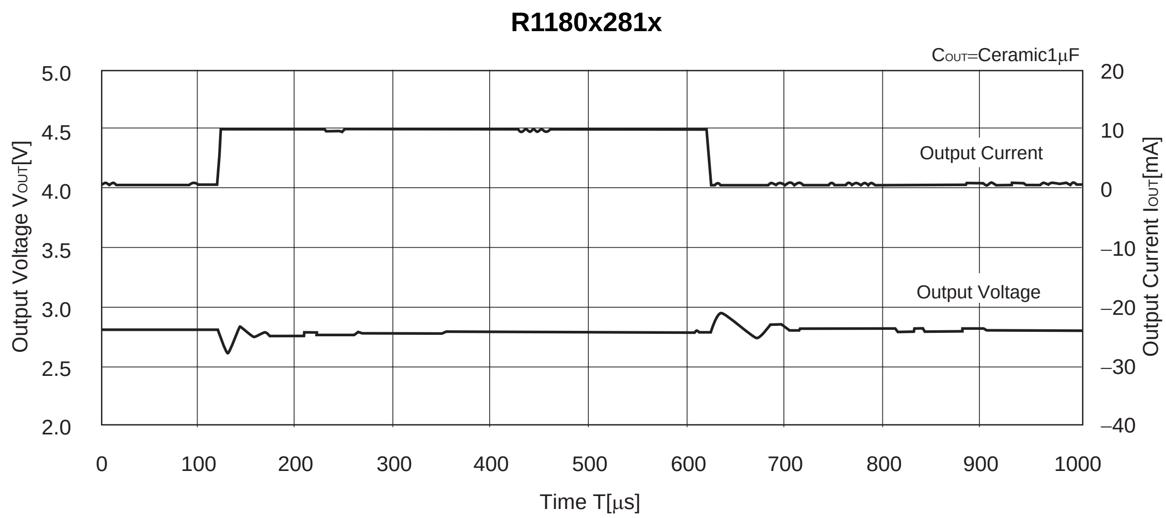
R1180x281x

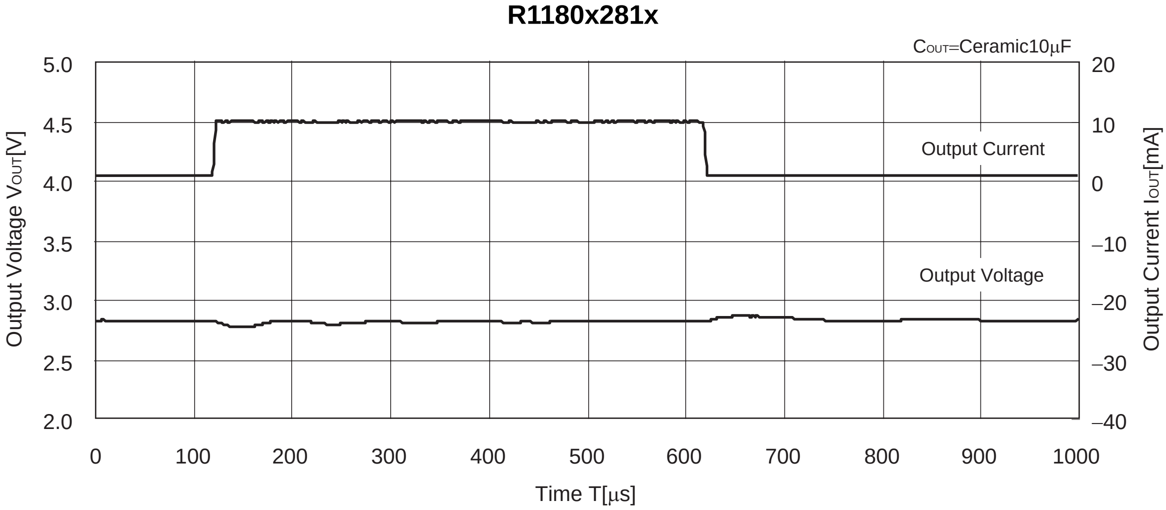


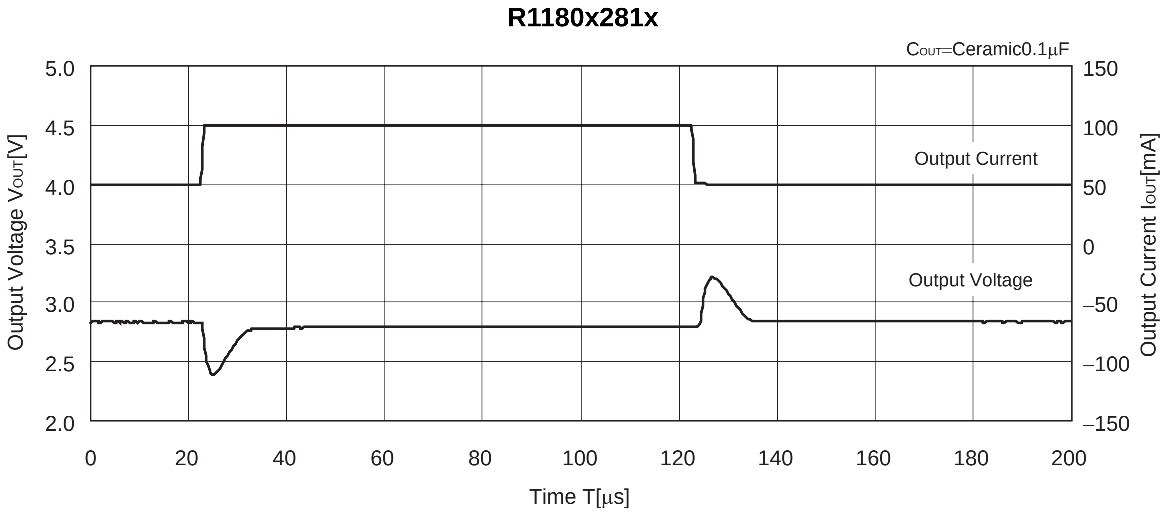


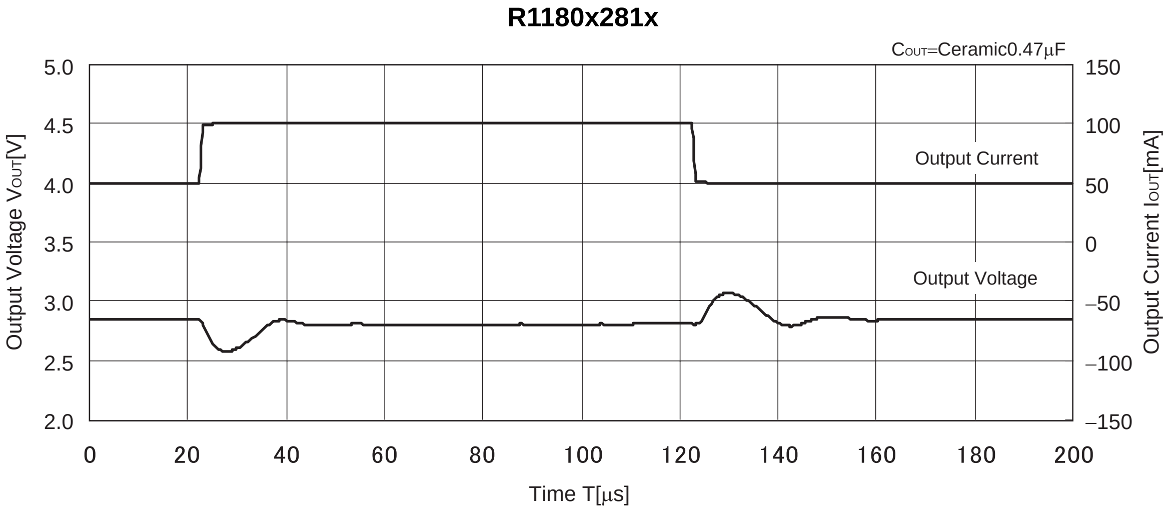
11) Load Transient Response (tr=tf=0.5μs V_{IN}=3.8V)



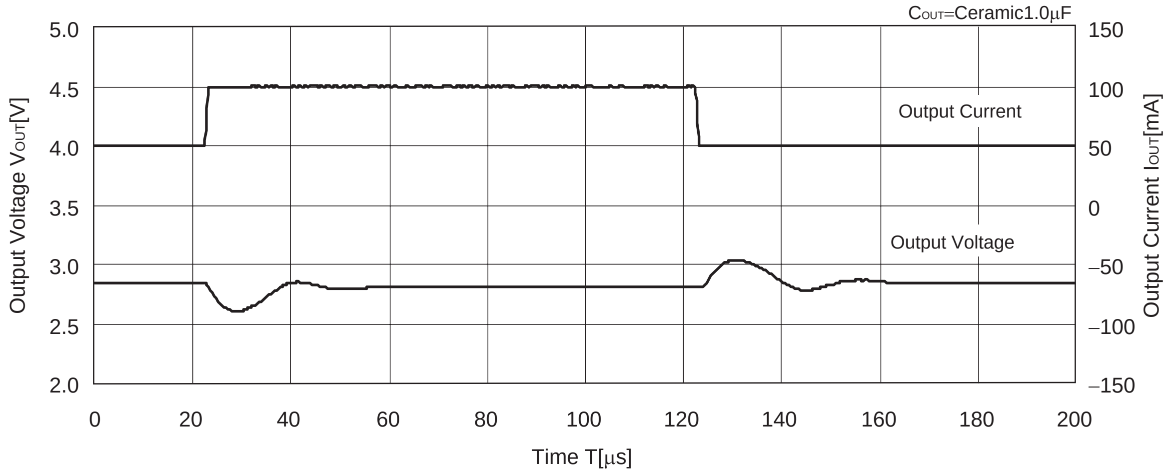



R1180x281x
COUT=Ceramic10μF
Output Current
Output Voltage
Output Voltage VOUT[V]
Output Current IOUT[mA]
5.0
4.5
4.0
3.5
3.0
2.5
2.0
20
10
0
−10
−20
−30
−40
0
100
200
300
400
500
600
700
800
900
1000
Time T[μs]


R1180x281x
COUT=Ceramic0.1μF
Output Current
Output Voltage
Output Voltage VOUT[V]
Output Current IOUT[mA]
5.0
4.5
4.0
3.5
3.0
2.5
2.0
150
100
50
0
−50
−100
−150
0
20
40
60
80
100
120
140
160
180
200
Time T[μs]


R1180x281x
COUT=Ceramic0.47μF
Output Current
Output Voltage
Output Voltage VOUT[V]
Output Current IOUT[mA]
5.0
4.5
4.0
3.5
3.0
2.5
2.0
150
100
50
0
−50
−100
−150
0
20
40
60
80
100
120
140
160
180
200
Time T[μs]

R1180x281x


COUT=Ceramic1.0μF
Output Current
Output Voltage
Output Voltage VOUT[V]
Output Current IOUT[mA]
Time T[μs]
5.0
4.5
4.0
3.5
3.0
2.5
2.0
150
100
50
0
−50
−100
−150
0
20
40
60
80
100
120
140
160
180
200

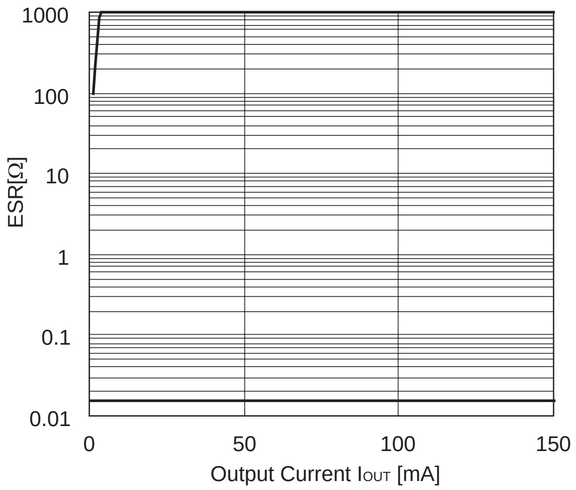
ESR vs. Output Current

The relations between I_{OUT} (Output Current) and ESR of an output capacitor are shown above. The conditions when the white noise level is under 40μV (Avg.) are marked as the hatched area in the graph.

<Measurement conditions>
(1) $V_{IN}=V_{OUT}+1V$
(2) Frequency Band: 10Hz to 2MHz (BW=30Hz)
(3) Temperature: –40°C to 85°C

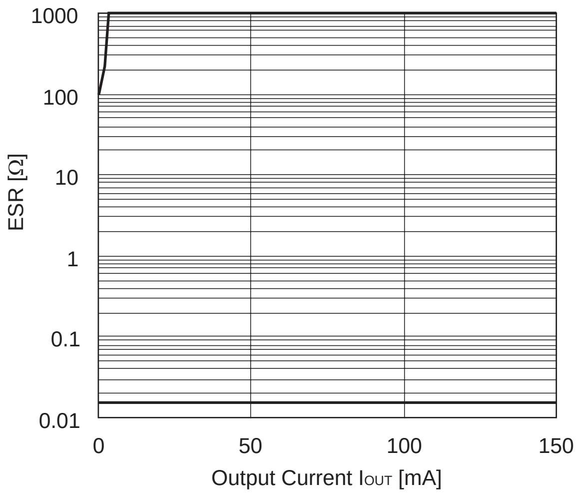
R1180x121x

C_{IN} =Ceramic 1.0μA, C_{OUT} =Ceramic 0.1μF



R1180x281x

C_{IN} =Ceramic 1.0μA, C_{OUT} =Ceramic 0.1μF



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