
STEP-UP DC/DC CONVERTER FOR WHITE LED BACK LIGHT

NO.EA-297-170620

OUTLINE

The R1206N071B is PWM control type step-up DC/DC converter IC with low supply current.

The R1206N071B is fully dedicated to drive White LEDs with constant current. Each of these ICs consists of an NMOS FET, an oscillator, a PWM comparator, a voltage reference unit, an error amplifier, a current limit circuit, an under voltage lockout circuit (UVLO), and an over-voltage protection circuit (OVP).

The R1206N071B can drive white LEDs in constant current with high efficiency by using an inductor, a diode, a resistor and capacitors as external components.

The LEDs current can be set by an external resistance value and can adjust the dimming of LEDs by CE pin according to the signal of PWM. Feedback voltage is 0.2V, therefore power loss by current setting resistance is small and efficiency is good. Maximum duty cycle is internally fixed, Typ. 91%. LEDs can be driven from low voltage. Protection circuits are the current limit of Lx peak current, the over voltage limit of output, and the under voltage lockout function.

It is controllable the dimming of LEDs quickly when the PWM signal (between 200Hz to 300kHz) input to CE pin. If the CE pin input is "L" in the fixed time (Typ. 0.5ms), the IC becomes the standby mode and turns OFF LEDs.

FEATURES

- Supply Current Typ. 500 μ A
- Standby Current Max. 5 μ A
- Input Voltage Range 1.8V to 5.5V
- Feedback Voltage 0.2V
- Feedback Voltage Accuracy $\pm 1.0\%$ (± 10 mV)
- Temperature-Drift Coefficient of Feedback Voltage ... ± 150 ppm/ $^{\circ}$ C
- Oscillator Frequency Typ. 1.2MHz
- Maximum Duty Cycle Typ. 91%
- Switch ON Resistance Typ. 1.35 Ω
- UVLO Detector Threshold Typ. 1.6V
- Lx Current Limit Protection Typ. 700mA
- OVP Detector Threshold Typ. 29.5V
- Switching Control PWM
- LED dimming control by external PWM signal (Frequency 200Hz to 300kHz)
- Packages SOT-23-6
- Ceramic capacitors are recommended 0.22 μ F

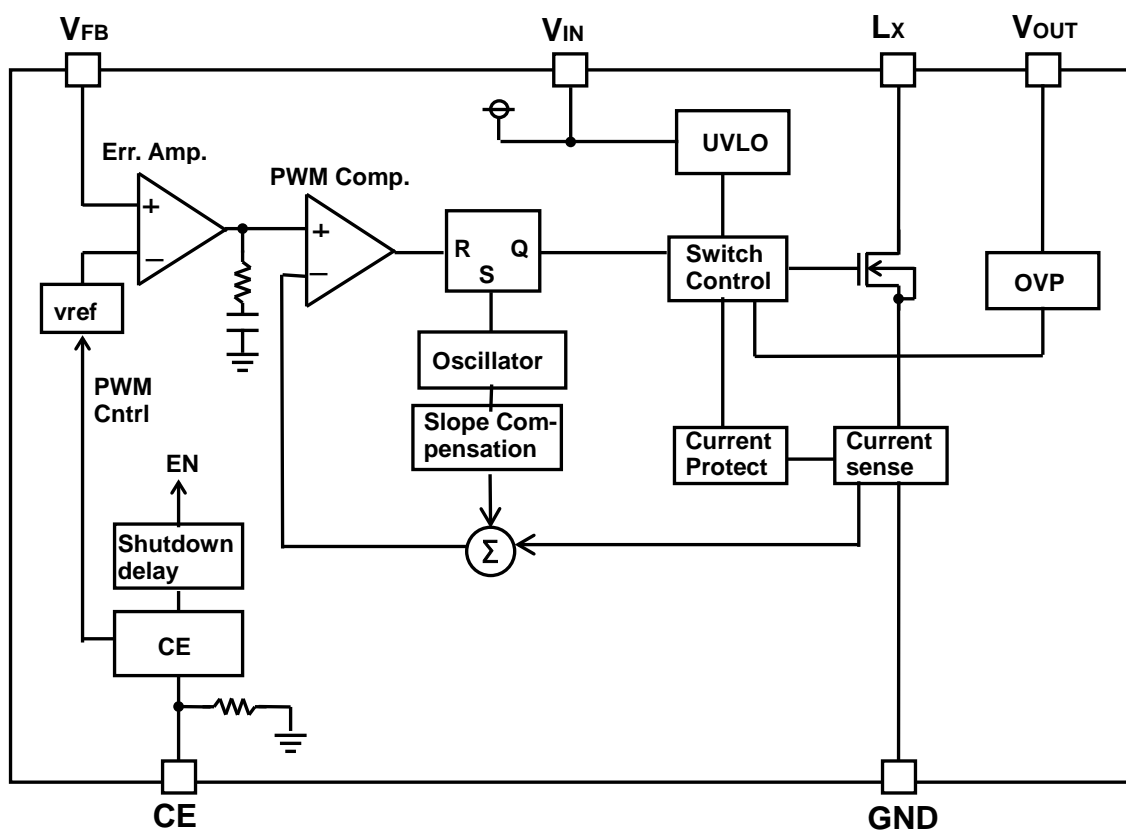
APPLICATIONS

- White LED Backlight for portable equipment

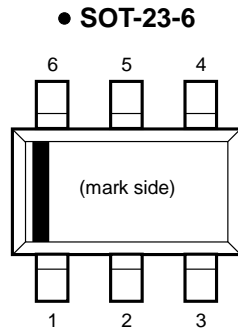
SELECTION GUIDE

| Product Name | Package | Quantity per Reel | Pb Free | Halogen Free |
|------------------|----------|-------------------|---------|--------------|
| R1206N071B-TR-FE | SOT-23-6 | 3,000 pcs | Yes | Yes |

BLOCK DIAGRAM



PIN DESCRIPTIONS



• SOT-23-6

| Pin No | Symbol | Pin Description |
|--------|------------------|-----------------------------------|
| 1 | L _X | Switching Pin (Open Drain Output) |
| 2 | GND | Ground Pin |
| 3 | V _{FB} | Feedback Pin |
| 4 | CE | Chip Enable Pin ("H" Active) |
| 5 | V _{OUT} | Output Pin |
| 6 | V _{IN} | Input Pin |

R1206N071B

NO.EA-297-170620

ABSOLUTE MAXIMUM RATINGS**(GND=0V)**

| Symbol | Item | Rating | Unit |
|-----------|----------------------------|----------------------|------|
| V_{IN} | V_{IN} Pin Voltage | -0.3 to 6.5 | V |
| V_{CE} | CE Pin Voltage | -0.3 to $V_{IN}+0.3$ | V |
| V_{FB} | V_{FB} Pin Voltage | -0.3 to $V_{IN}+0.3$ | V |
| V_{OUT} | V_{OUT} Pin Voltage | -0.3 to 32 | V |
| V_{LX} | L_x Pin Voltage | -0.3 to 32 | V |
| I_{LX} | L_x Pin Current | 1000 | mA |
| P_D | Power Dissipation * | 420 | mW |
| T_j | Junction Temperature Range | -40 to 125 | °C |
| T_{stg} | Storage Temperature Range | -55 to 125 | °C |

*) Refer to *POWER DISSIPATION* for detailed information.**ABSOLUTE MAXIMUM RATINGS**

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

RECOMMENDED OPERATING CONDITIONS

| Symbol | Item | Rating | Unit |
|----------|-----------------------------|------------|------|
| V_{IN} | Operating Input Voltage | 1.8 to 5.5 | V |
| T_a | Operating Temperature Range | -40 to 85 | °C |

RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

ELECTRICAL CHARACTERISTICS

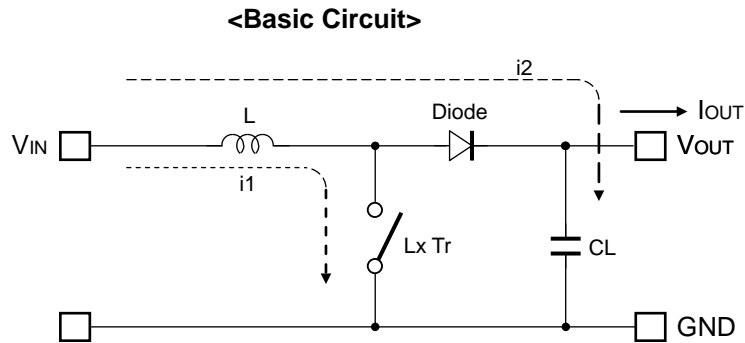
• R1206N071B

(Ta=25°C)

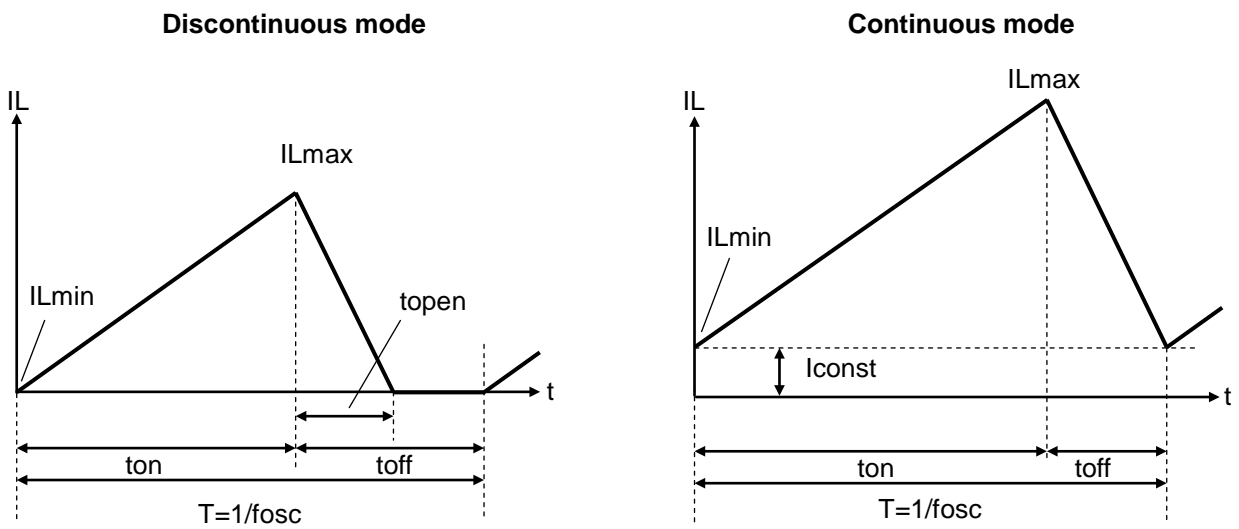
| Symbol | Item | Conditions | Min. | Typ. | Max. | Unit |
|--------------------------------------|---------------------------------------------------|-----------------------------------------------------------------------|------|----------------------------|------|--------|
| I _{DD} | Supply Current | V _{IN} =5.5V, V _{FB} =0V, Lx at no load | | 0.5 | 1.0 | mA |
| I _{standby} | Standby Current | V _{IN} =5.5V, V _{CE} =0V | | 1.0 | 5.0 | μA |
| V _{UVLO1} | UVLO Detector Threshold | V _{IN} falling | 1.5 | 1.6 | 1.7 | V |
| V _{UVLO2} | UVLO Released Voltage | V _{IN} rising | | V _{UVLO1} +0.1 | 1.8 | V |
| V _{CEH} | CE Input Voltage "H" | V _{IN} =5.5V | 1.5 | | | V |
| V _{CEL} | CE Input Voltage "L" | V _{IN} =1.8V | | | 0.5 | V |
| R _{CE} | CE Pull Down Resistance | V _{IN} =3.6V | 600 | 1200 | 2200 | kΩ |
| V _{FB} | V _{FB} Voltage Accuracy | V _{IN} =V _{CE} =3.6V | 0.19 | 0.20 | 0.21 | V |
| $\frac{\Delta V_{FB}}{\Delta T_a}$ | V _{FB} Voltage Temperature Coefficient | V _{IN} =V _{CE} =3.6V, -40°C ≤ T _a ≤ 85°C | | ±150 | | ppm/°C |
| I _{FB} | V _{FB} Input Current | V _{IN} =5.5V, V _{FB} =0V or V _{IN} | -0.1 | | 0.1 | μA |
| R _{ON} | Switch ON Resistance | V _{IN} =3.6V, I _{LX} =100mA | | 1.35 | | Ω |
| I _{LXleak} | Switch Leakage Current | V _{LX} =30V | | 0 | 3.0 | μA |
| I _{LXlim} | Switch Current Limit | V _{IN} =3.6V | 400 | 700 | 1000 | mA |
| f _{osc} | Oscillator Frequency | V _{IN} =3.6V, V _{OUT} =V _{FB} =0V | 1.0 | 1.2 | 1.4 | MHz |
| Maxduty | Maximum Duty Cycle | V _{IN} =3.6V, V _{OUT} =V _{FB} =0V | 86 | 91 | | % |
| V _{OVP1} | OVP Detector Threshold | V _{IN} =3.6V, V _{OUT} rising | 28.7 | 29.5 | 30.3 | V |
| $\frac{\Delta V_{OVP1}}{\Delta T_a}$ | V _{OVP1} Voltage Temperature Coefficient | V _{IN} =V _{CE} =3.6V, -40°C ≤ T _a ≤ 85°C | | ±150 | | ppm/°C |
| V _{OVP2} | OVP Released Voltage | V _{IN} =3.6V, V _{OUT} falling | | V _{OVP1} -1.55 | | V |

OPERATING DESCRIPTIONS

OPERATION OF STEP-UP DC/DC CONVERTER AND OUTPUT CURRENT



<Current through L>



There are two operation modes of the step-up PWM control-DC/DC converter. That is the continuous mode and discontinuous mode by the continuousness inductor.

When the transistor turns ON, the voltage of inductor L becomes equal to V_{IN} voltage. The increase value of inductor current ($i1$) will be

$$\Delta i1 = V_{IN} \times t_{on} / L \dots\dots\dots \text{Formula 1}$$

As the step-up circuit, during the OFF time (when the transistor turns OFF) the voltage is continually supply from the power supply. The decrease value of inductor current ($i2$) will be

$$\Delta i2 = (V_{OUT} - V_{IN}) \times t_{open} / L \dots\dots\dots \text{Formula 2}$$

At the PWM control-method, the inductor current become continuously when $t_{open}=t_{off}$, the DC/DC converter operate as the continuous mode.

In the continuous mode, the variation of current of i_1 and i_2 is same at regular condition.

$$V_{IN} \times t_{on} / L = (V_{OUT} - V_{IN}) \times t_{off} / L \dots\dots\dots \text{Formula 3}$$

The duty at continuous mode will be

$$\text{Duty} = t_{on} / (t_{on} + t_{off}) = (V_{OUT} - V_{IN}) / V_{OUT} \dots\dots\dots \text{Formula 4}$$

The average of inductor current at $t_{open}=t_{off}$ will be

$$I_{L(\text{Ave.})} = V_{IN} \times t_{on} / (2 \times L) \dots\dots\dots \text{Formula 5}$$

If the input power is equal to the output power, the I_{OUT} will be

$$I_{OUT} = V_{IN}^2 \times t_{on} / (2 \times L \times V_{OUT}) \dots\dots\dots \text{Formula 6}$$

If the I_{OUT} value is large than above the calculated value (Formula 6), it will become the continuous mode, At this status, the peak current (I_{Lmax}) of inductor will be

$$I_{Lmax} = I_{OUT} \times V_{OUT} / V_{IN} + V_{IN} \times t_{on} / (2 \times L) \dots\dots\dots \text{Formula 7}$$

$$I_{Lmax} = I_{OUT} \times V_{OUT} / V_{IN} + V_{IN} \times T \times (V_{OUT} - V_{IN}) / (2 \times L \times V_{OUT}) \dots\dots\dots \text{Formula 8}$$

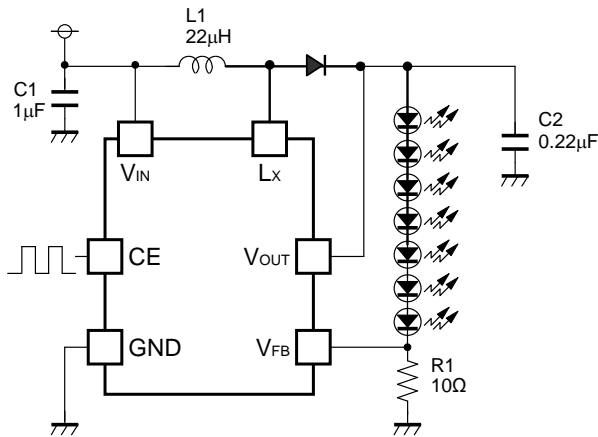
The peak current value is larger than the I_{OUT} value. In case of this, selecting the condition of the input and the output and the external components by considering of I_{Lmax} value.

The explanation above is based on the ideal calculation, and the loss caused by L_x switch and the external components are not included.

The actual maximum output current will be between 50% and 80% by the above calculations. Especially, when the I_L is large or V_{IN} is low, the loss of V_{IN} is generated with on resistance of the switch. Moreover, it is necessary to consider V_F of the diode (approximately 0.8V) about V_{OUT} .

APPLICATION INFORMATION

• Typical Application Circuit



| | |
|----|---------------|
| C1 | CM105B105K06 |
| C2 | GRM21BR71H224 |
| L1 | LQH32CN220K53 |

• LED Current setting

When CE pin input is "H" (Duty=100%), LED current can be set with feedback resistor (R1)

$$I_{LED} = V_{FB} / R1$$

• LED Dimming Control

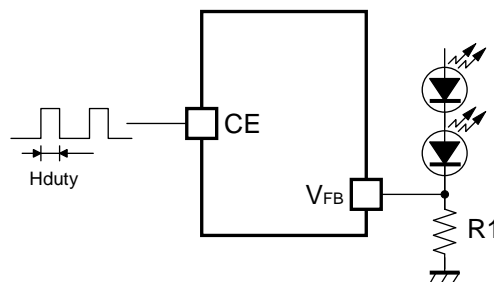
The LED brightness can be controlled by inputting the PWM signal to the CE pin. If the CE pin input is "L" in the fixed time (Typ.0.5ms), the IC becomes the standby mode and turns OFF LEDs.

The current of LEDs when the CE pin is "H" input (Duty=100%) is shown by the above expression. The current of LEDs can be controlled by Duty of the PWM signal of the input CE pin. The current of LEDs when High-Duty of the CE input is Hduty reaches the value as calculatable following formula.

$$I_{LED} = Hduty \times V_{FB} / R1$$

The frequency of the PWM signal is using the range between 200Hz to 300kHz.

When controlling the LED brightness by the PWM signal of 20kHz or less; The increasing or decreasing of the inductor current might be make a sounds in the hearable sound wave area. In that case, please use the PWM signal in the high frequency area.



Dimming control by CE pin input

- **Soft-Start**

The output of the error amplifier starts from 0V and the inrush current is suppressed when starting by the CE pin "H" input.

Moreover, the inrush current can be suppressed by gradually enlarging Duty of the PWM signal to the CE pin.

- **Selection of Inductors**

The peak current of the inductor at normal mode can be calculated as next formula:

$$I_{Lmax}=1.25 \times I_{LED} \times V_{OUT} / V_{IN} + 0.5 \times V_{IN} \times (V_{OUT} - V_{IN}) / (L \times V_{OUT} \times f_{osc})$$

When the start-up or dimming control by CE pin, transient current flows, the peak current must be equal or less than the current limit of the IC. The peak current should not beyond the rating current of the inductor.

When 4-7LEDs are driven with $V_{IN}=3.6V$, the recommended inductance value is $10\mu H$ - $22\mu H$.

- **Selection of Capacitors**

Set $1\mu F$ or more value bypass capacitor C1 between VIN pin and GND pin as close as possible.

Set $0.22\mu F$ or more capacitor C2 between VOUT pin and GND pin.

TECHNICAL NOTES

● Current path on PCB

The current paths in an application circuit are shown in Fig.2 and 3.

A current flows through the paths shown in Fig.2 at the time of MOSFET-ON, and shown in Fig.3 at the time of MOSFET-OFF. In the paths pointed with red arrows in Fig.3, current flows just in MOSFET-ON period or just in MOSFET-OFF period. Parasitic impedance / inductance and the capacitance of these paths influence stability of the system and cause noise outbreak. So please minimize this side effect. In addition, please shorten the wiring of other current paths shown in Fig.2 and 3 except for the paths of LED load.

● LAYOUT Guide for PCB

- Please shorten the wiring of the input capacitor(C1) between V_{IN} pin and GND pin of IC. The GND pin should be connected to the strong GND plane.
- The area of L_x land pattern should be smaller.
- The wiring between L_x pin and inductor and diode should be short and please put output capacitor(C2) close to the cathode of diode.
- Please make the GND side of output capacitor(C2) close to the GND pin of IC.

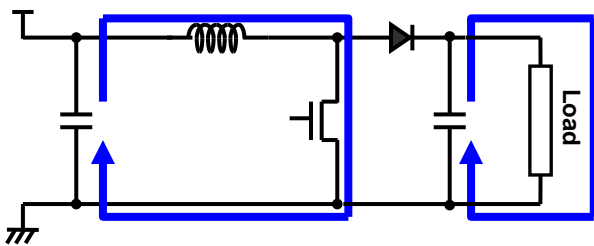


Fig.2 MOSFET-ON

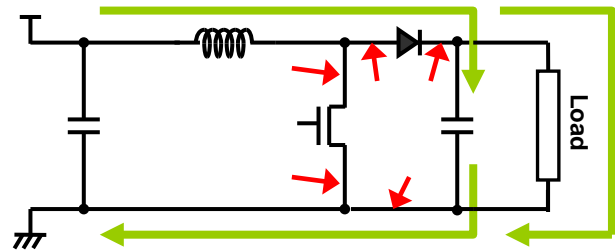
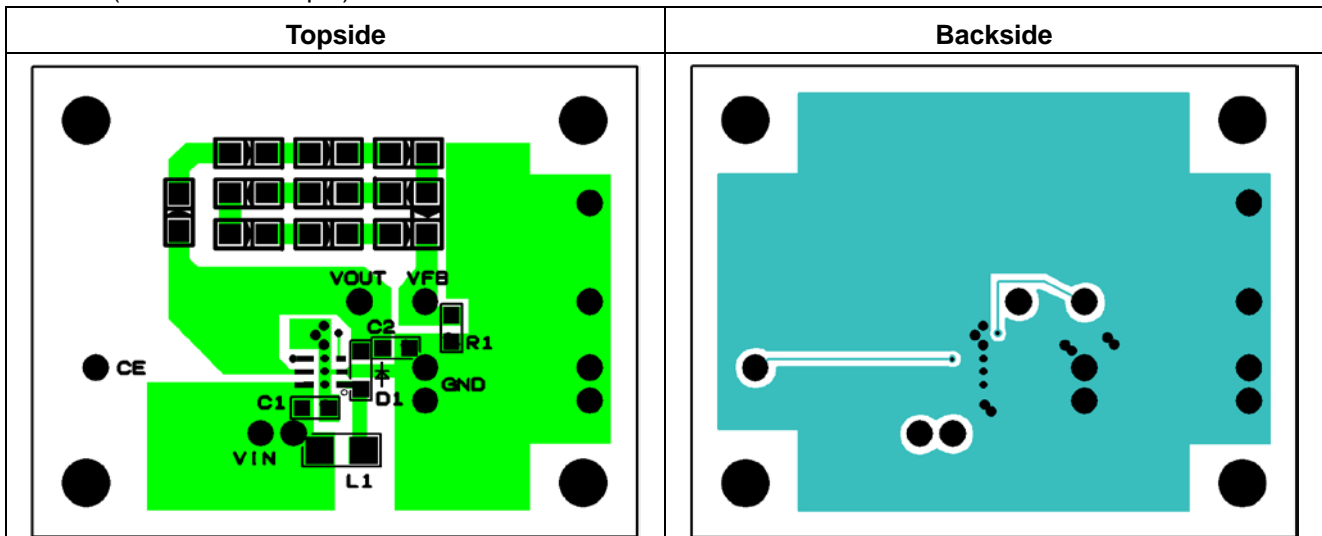


Fig.3 MOSFET-OFF

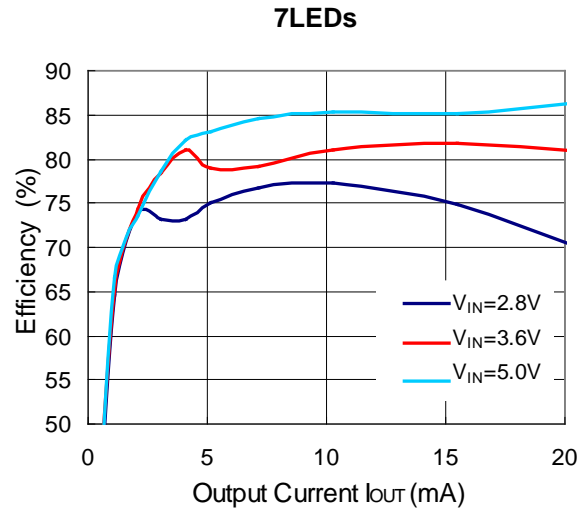
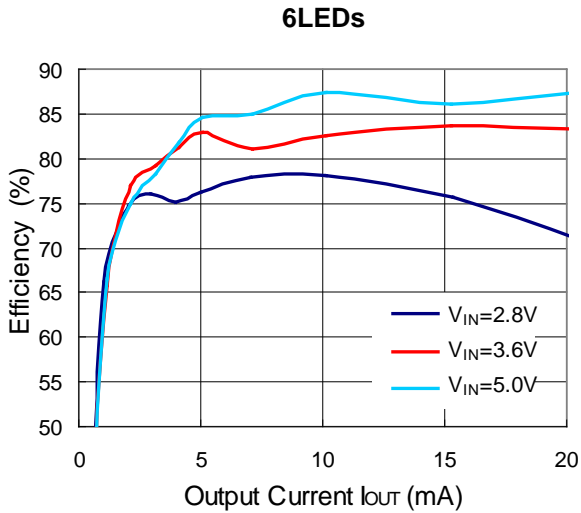
● PCB Layout

R1206N (PKG: SOT-23-6pin)

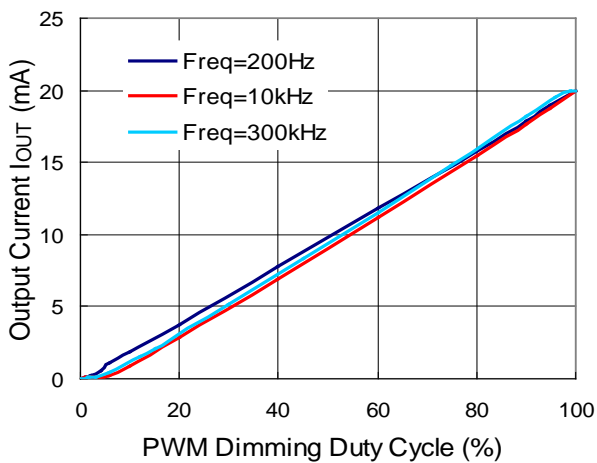


TYPICAL CHARACTERISTICS

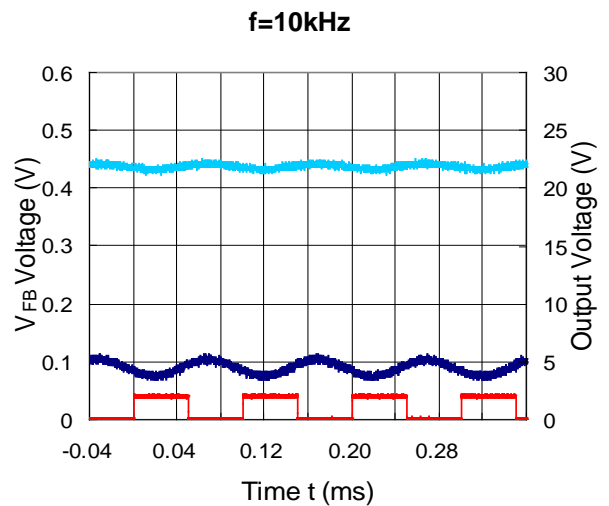
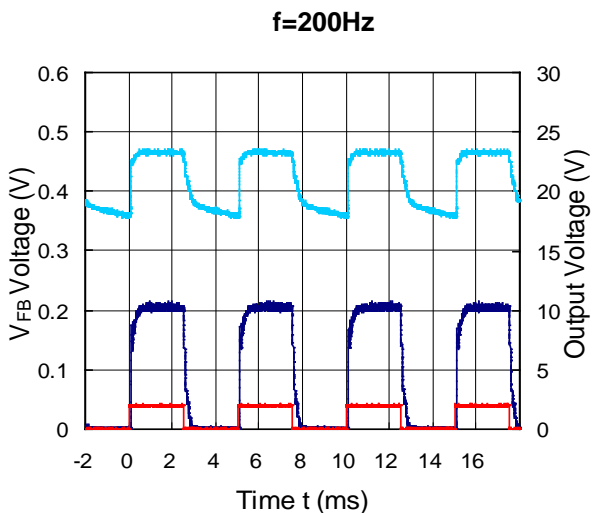
1) Efficiency vs. Output Current Characteristics



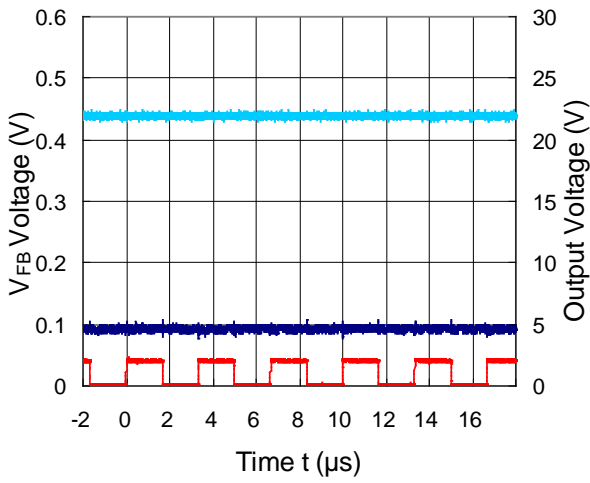
2) PWM Dimming Duty Cycle vs. Output Current($R1=10\Omega$)



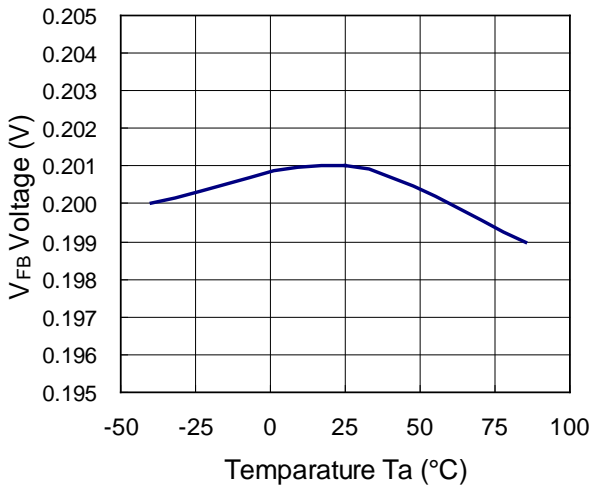
3) Output Current Ripple during PWM Dimming



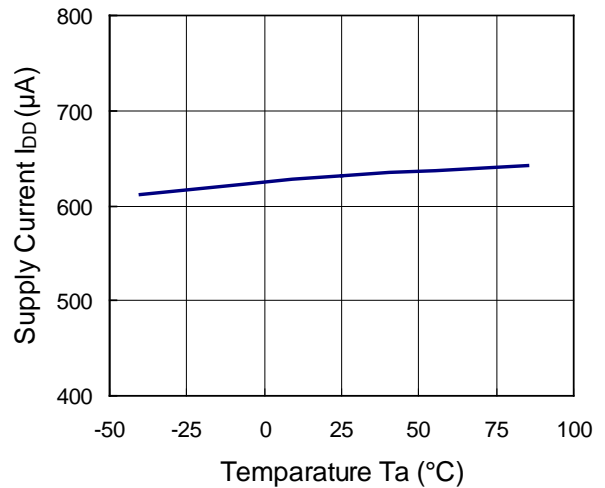
f=300kHz



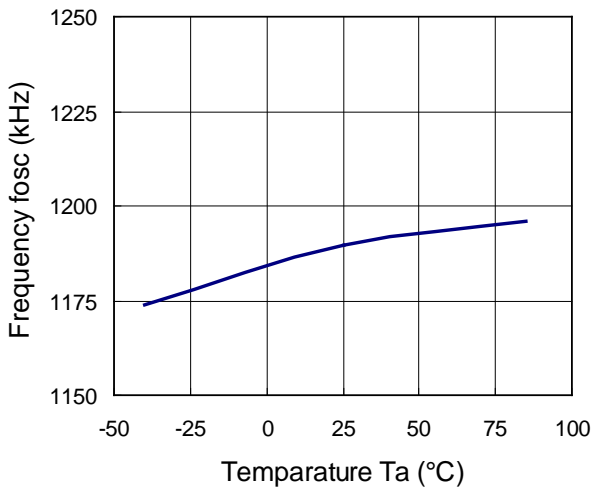
4) VFB Voltage vs. Temperature



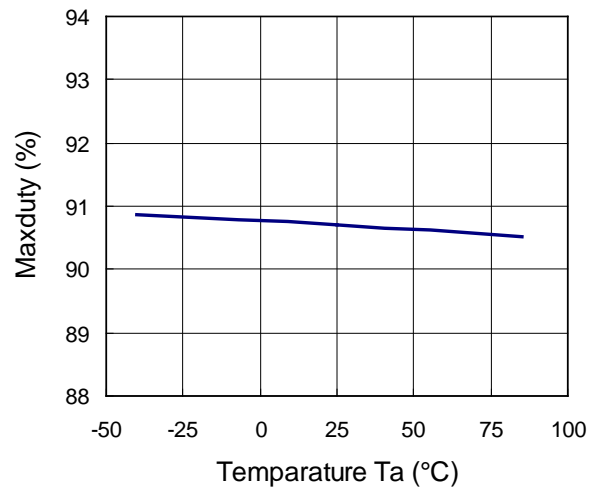
5) Supply Current vs. Temperature



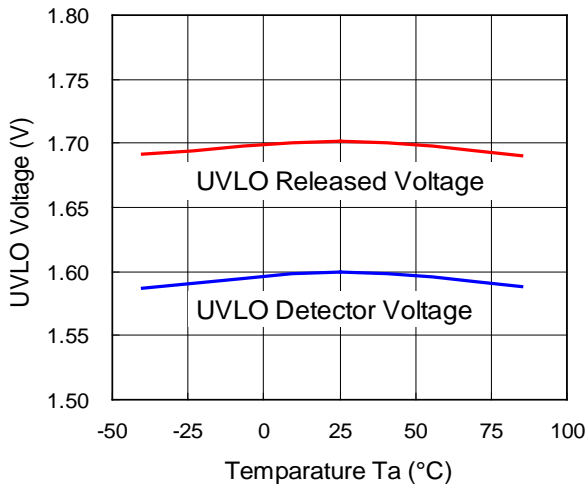
6) Oscillator Frequency vs. Temperature



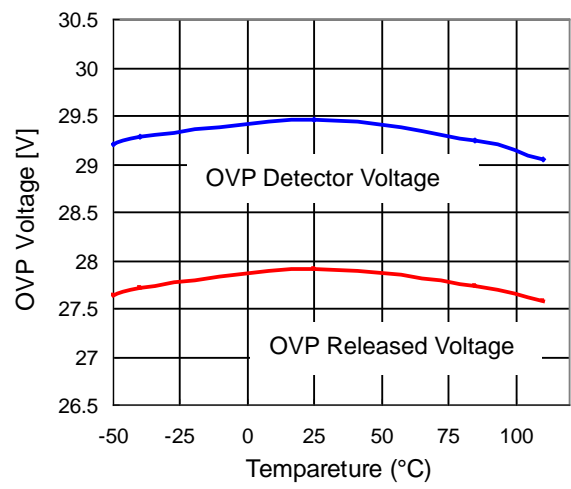
7) Maxduty vs. Temperature



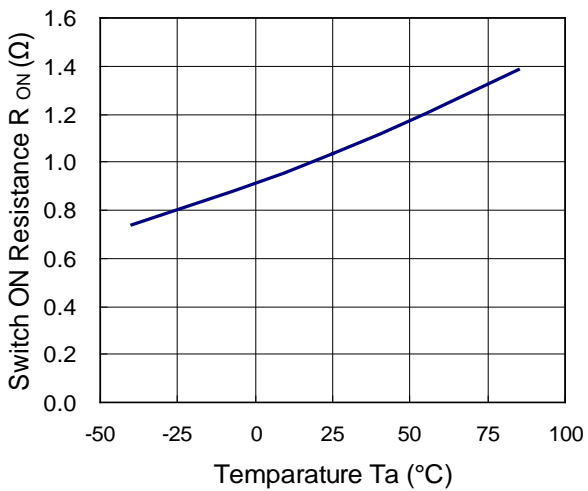
8) UVLO Output Voltage vs. Temperature



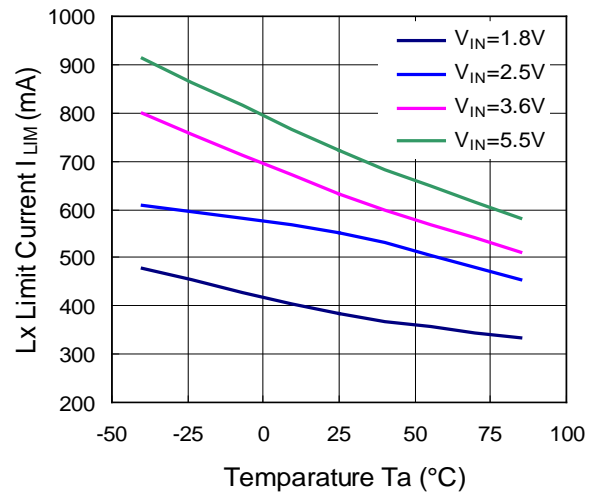
9) OVP Voltage vs. Temperature



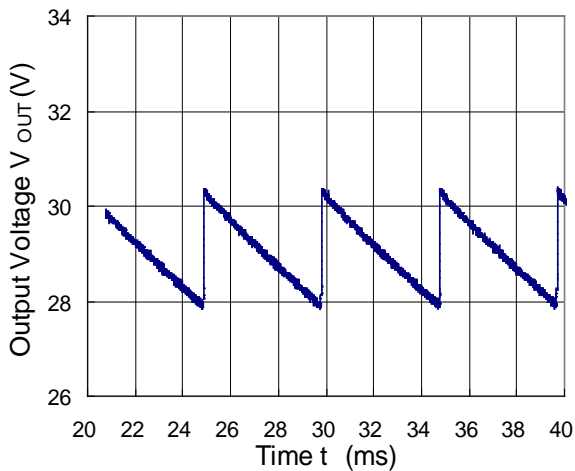
10) Switch ON Resistance vs. Temperature



11) Lx Current Limit vs. Temperature



12) OVP Operating Output Voltage Waveform



The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following conditions are used in this measurement.

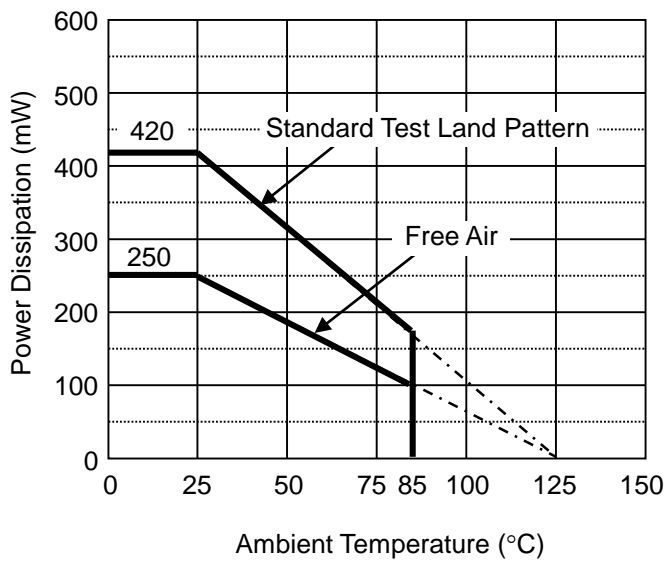
Measurement Conditions

| | Standard Test Land Pattern |
|------------------|---------------------------------------------------|
| Environment | Mounting on Board (Wind Velocity = 0 m/s) |
| Board Material | Glass Cloth Epoxy Plastic (Double-Sided Board) |
| Board Dimensions | 40 mm x 40 mm x 1.6 mm |
| Copper Ratio | Top Side: Approx. 50% Bottom Side: Approx. 50% |
| Through-holes | φ 0.5 mm x 44 pcs |

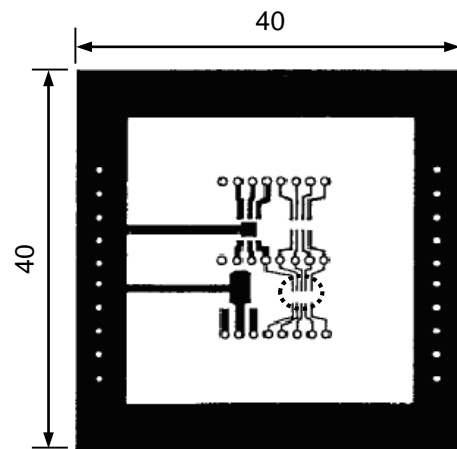
Measurement Result

(Ta = 25°C, Tjmax = 125°C)

| | Standard Test Land Pattern | Free Air |
|--------------------|---------------------------------------------------------------------------------|-----------------|
| Power Dissipation | 420 mW | 250 mW |
| Thermal Resistance | $\theta_{ja} = (125 - 25^\circ\text{C}) / 0.42 \text{ W} = 238^\circ\text{C/W}$ | 400°C/W |

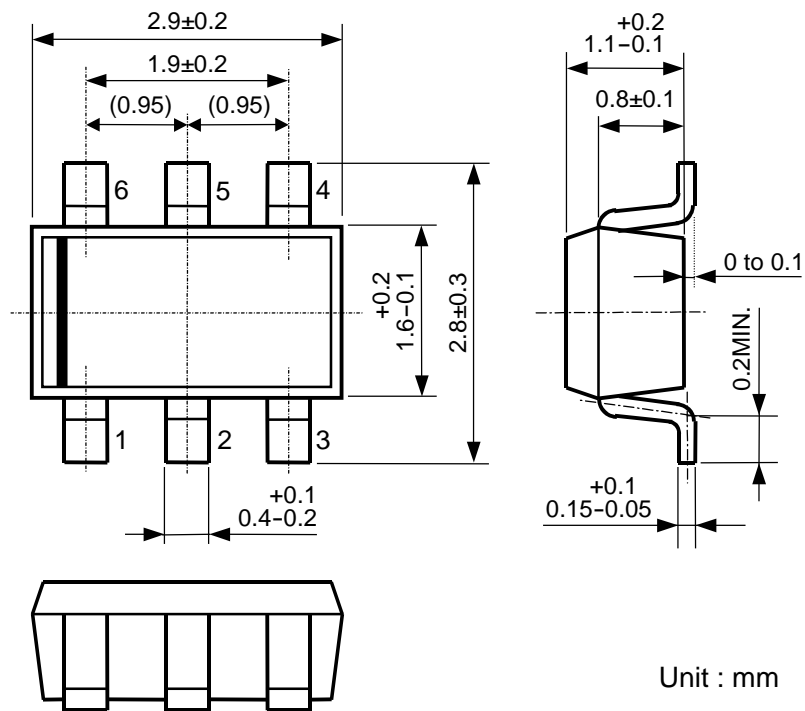


Power Dissipation vs. Ambient Temperature



○ IC Mount Area (mm)

Measurement Board Pattern



SOT-23-6 Package Dimensions



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